

## 1. Research Context

- Loops are the main source of parallelism in computationally-intensive scientific applications
- Scientific applications performance on high performance computing (HPC) systems may be degraded due to load imbalance

### Challenges

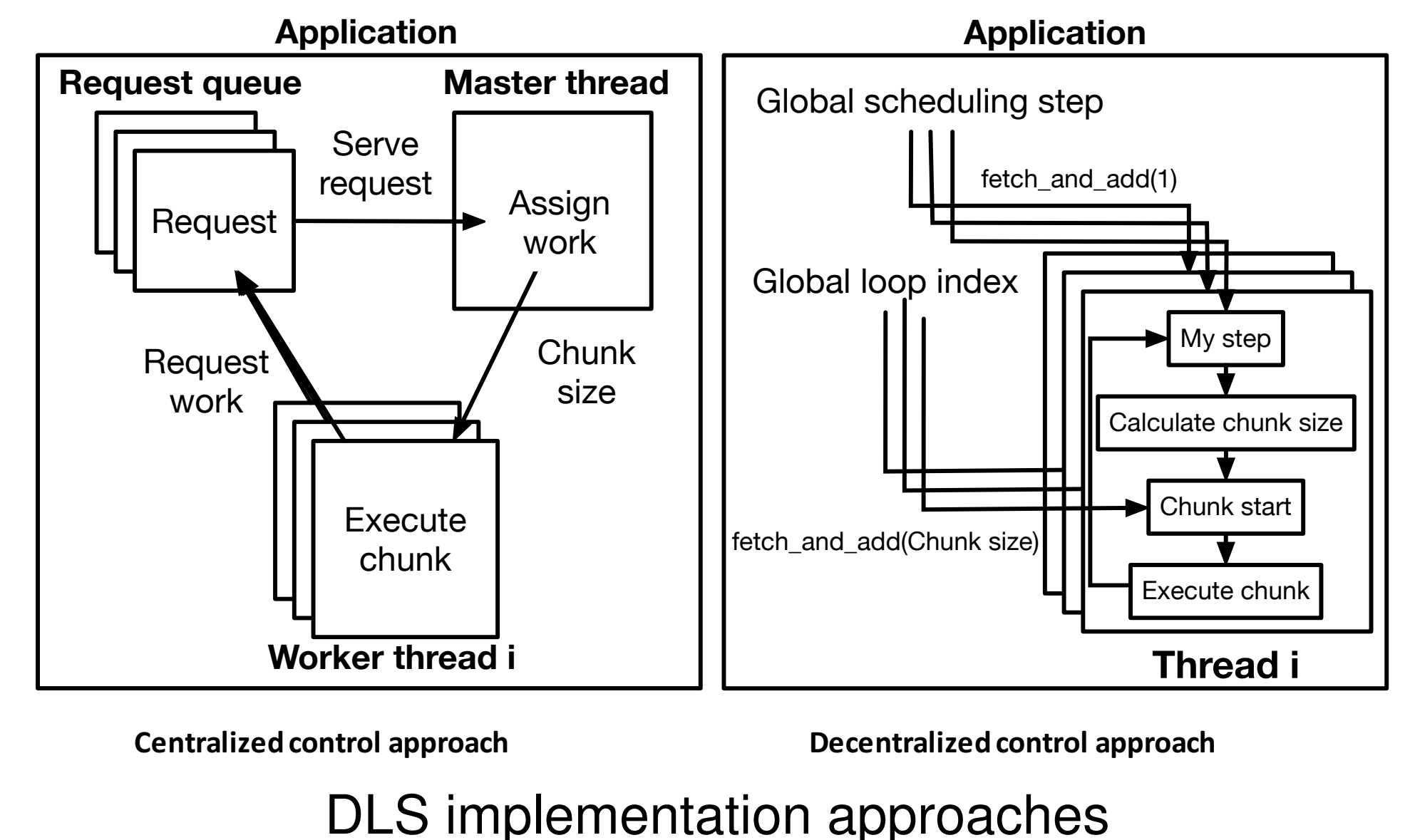
- Load imbalance may be caused by irregular computational load per loop iteration, or irregular and unpredictable computing system characteristics
  - Dynamic loop scheduling techniques are used to address load imbalance in computationally-intensive applications
- Perturbations and failures are expected to manifest increasingly in future HPC systems, with high count of processing elements (nodes, sockets, cores, ...)

### Goal

Improve computationally-intensive scientific applications performance on HPC systems under unpredictable application and system characteristics via **robust scheduling**

## 2. Dynamic Loop Scheduling Techniques

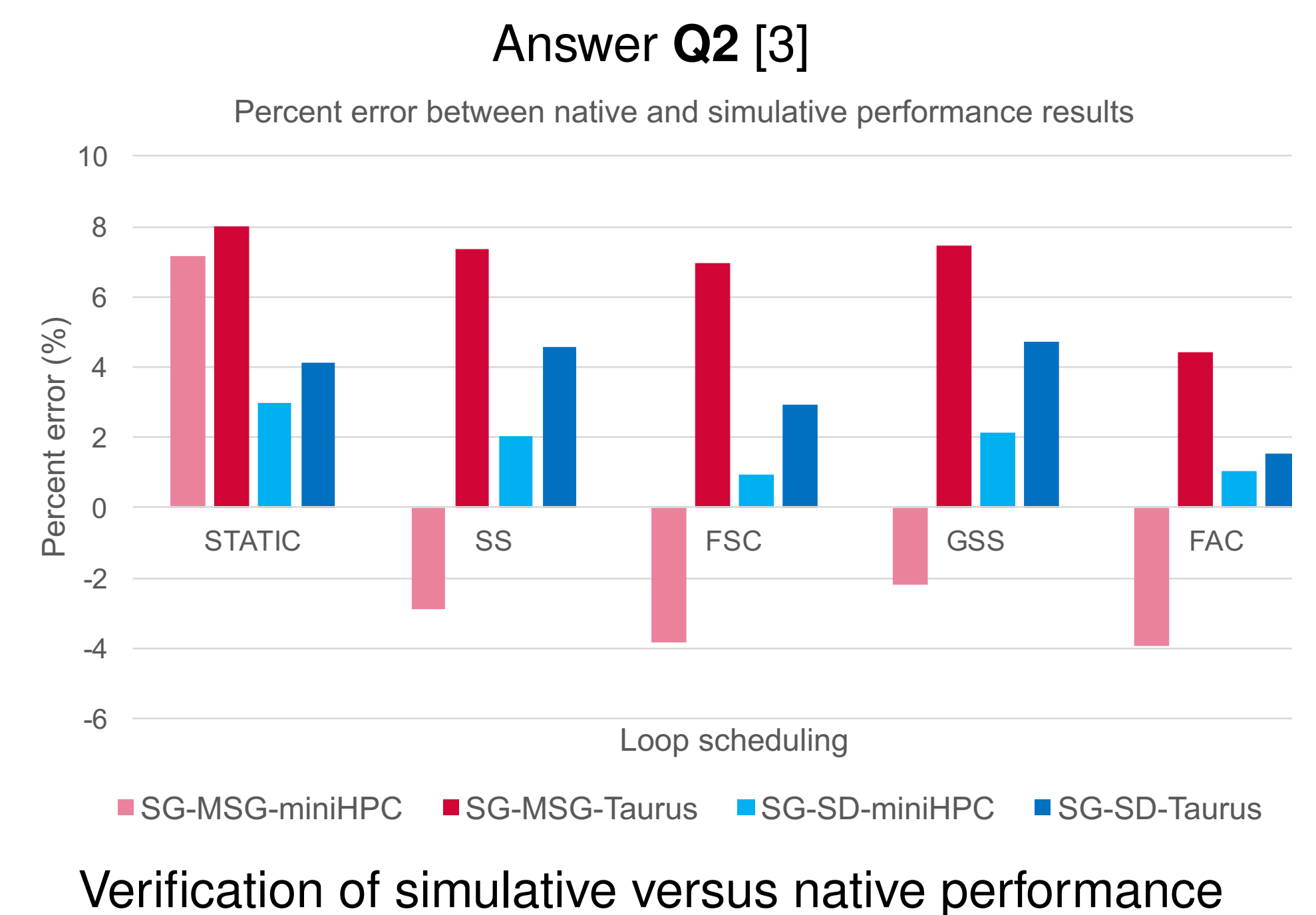
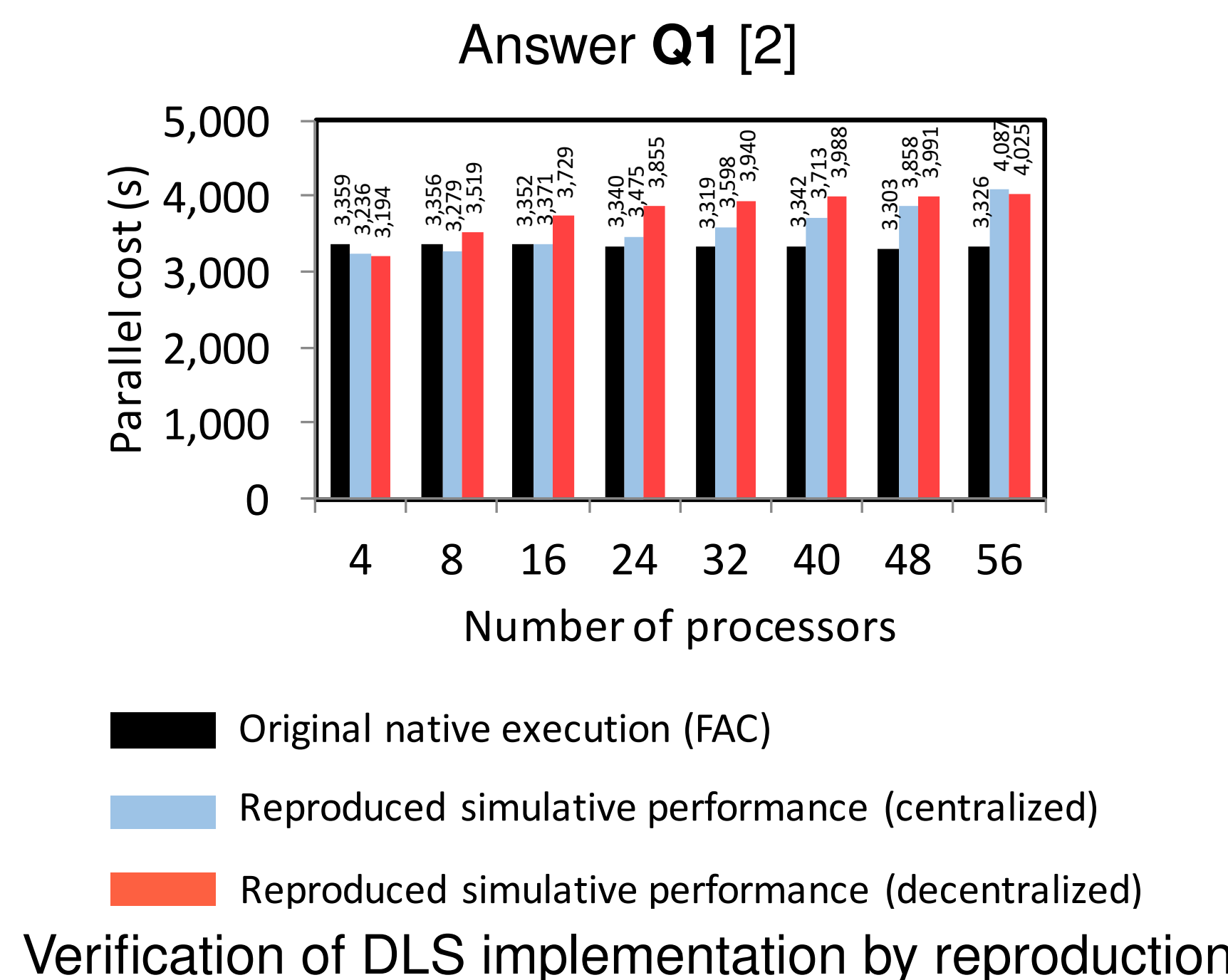
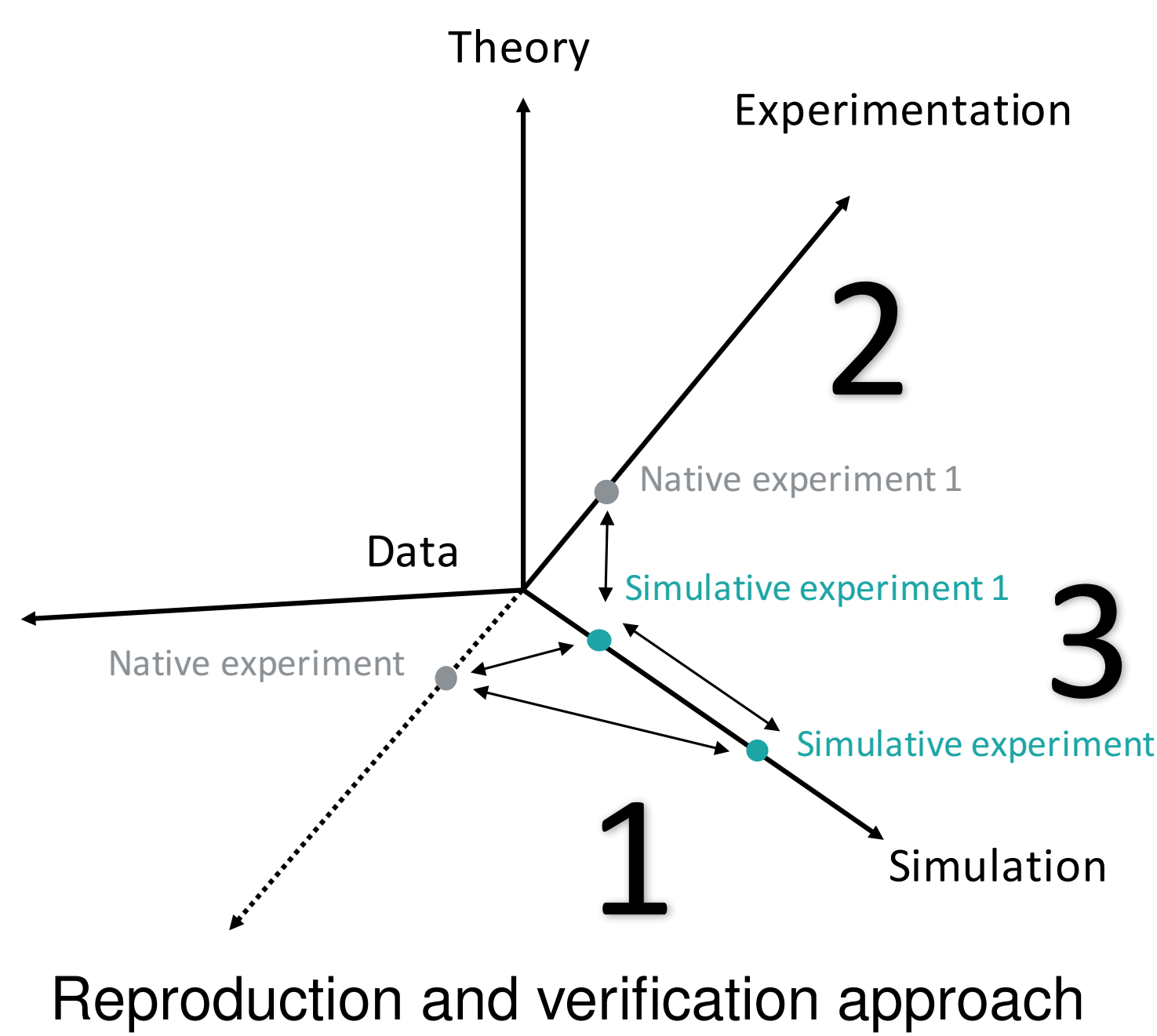
Scheduling technique	Loop scheduling characteristics [1]						Use of batches		
	Category			Chunk calculation		Chunk size		Yes	No
	Static	Nonadaptive	Adaptive	Deterministic	Probabilistic	Fixed	Variable		
Static block cyclic (STATIC)	✓			✓		✓			
Self-scheduling (SS)		✓		✓		✓			
Fixed size chunking (FSC)		✓		✓		✓			
Guided self-scheduling (GSS)		✓		✓		✓			
Factoring (FAC)		✓		✓		✓			
Weighted factoring (WF)		✓		✓		✓			
Adaptive weighted factoring (AWF-B)		✓	✓	✓		✓			
Adaptive weighted factoring (AWF-C)		✓	✓	✓		✓			
Adaptive weighted factoring (AWF-D)		✓	✓	✓		✓			
Adaptive weighted factoring (AWF-E)		✓	✓	✓		✓			
Adaptive factoring (AF)		✓	✓	✓		✓			



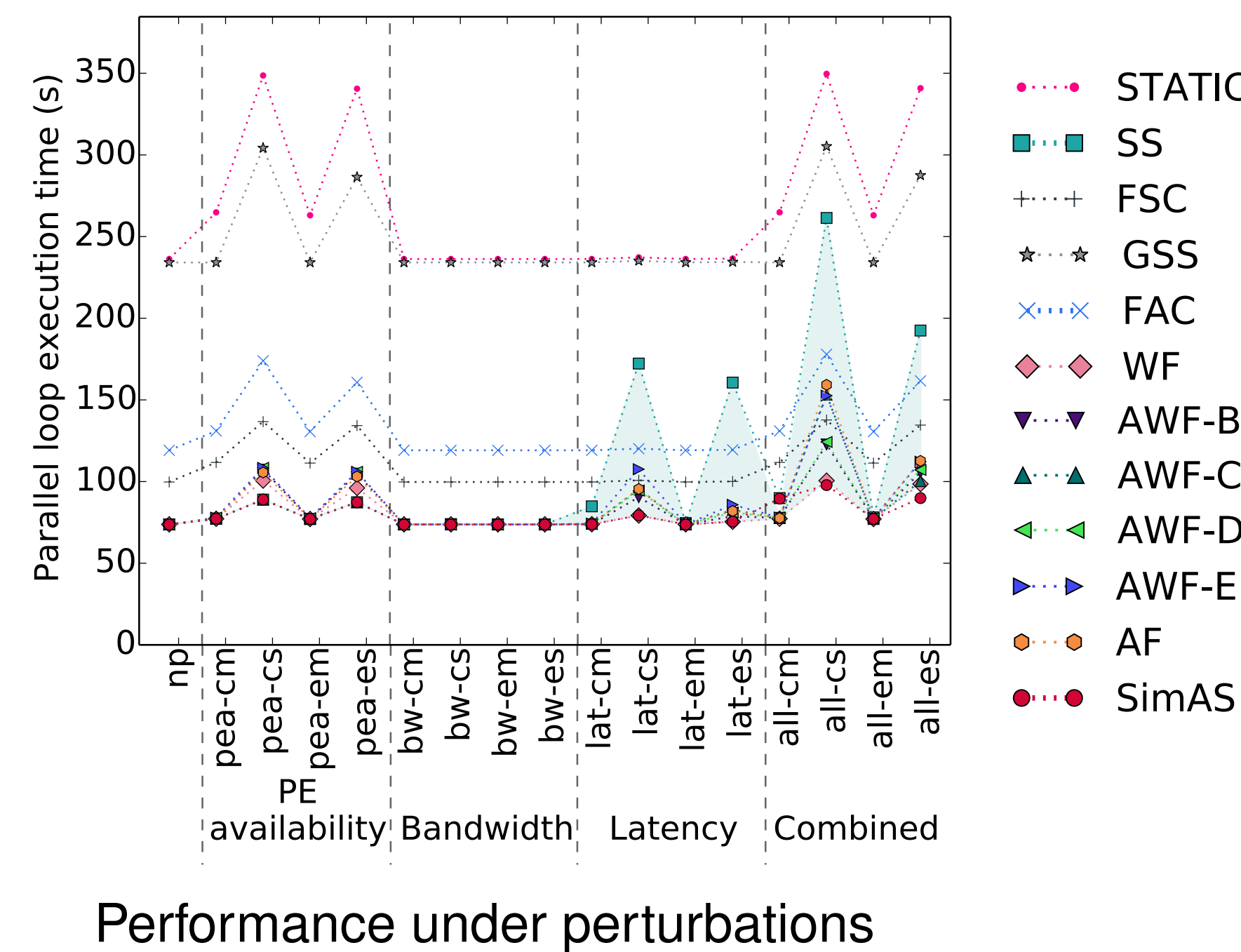
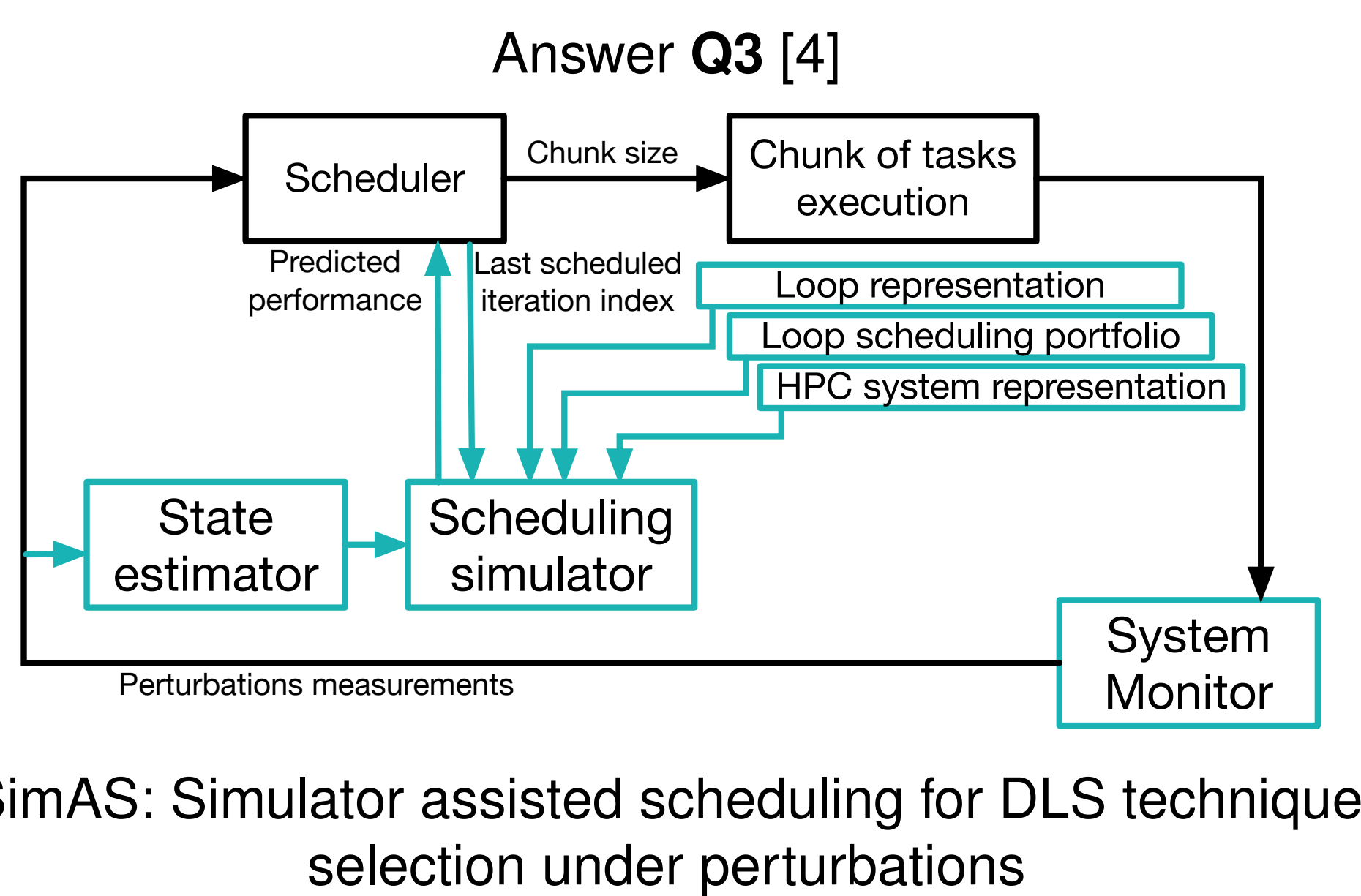
## 3. Research Questions

- How close are the implementations of DLS techniques in simulation and native codes to their original proposed specifications decades ago?
- How realistic are the simulations of executions of scientific applications using DLS on HPC platforms?
- Given an application, a high-performance computing (HPC) system, and both their characteristics and interplay, which DLS technique will achieve improved performance under unpredictable perturbations?
- How to tolerate fail-stop failures of PEs during execution and maintain a balanced load enhanced application performance?
- How to ensure applications results integrity under silent data corruption (SDC) faults?

## 4. How Realistic are Simulations of Performance?



## 5. SimAS: Simulator Assisted Scheduling Under Perturbations



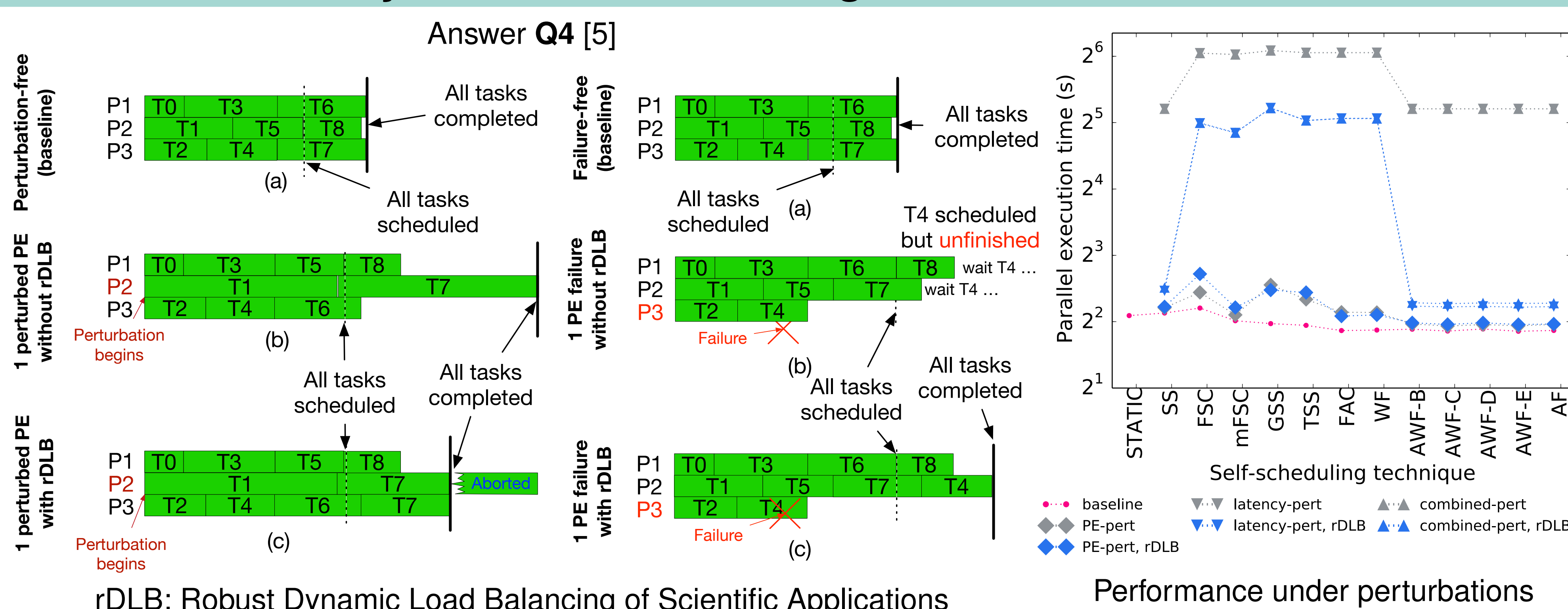
## 7. Next Steps

- Address SDC by replicating computations and solve scheduling challenges of replication (answer Q5)
- Analyze load imbalance in scientific applications at multiple levels of software parallelism
- Examining the performance of DLS at large scale via simulations
- Exploit multilevel scheduling to achieve fault tolerant application execution

## Acknowledgment

This work is supported by the Swiss Platform for Advanced Scientific Computing (PASC) project SPH-EXA: Optimizing Smooth Particle Hydrodynamics for Exascale Computing

## 6. rDLB: Robust Dynamic Load Balancing



## References

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