# **Design of Robust Scheduling Methodologies in** High Performance Computing University



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## **1. Research Context**

of Basel

- Loops are the main source of parallelism in computationally-intensive scientific applications
- Scientific applications performance on high performance computing (HPC) systems may be degraded due to load imbalance

#### Challenges

• Load imbalance may be caused by irregular compu-

## 2. Dynamic Loop Scheduling Techniques

Loop sche	duling cha	aracteristics	[1]	_ Application	Application
Scheduling technique	Category	Chunk calculatior	Chunk Use of size batches	Request queue Master thread	Global scheduling step
	Dyna			_     [ Serve	
	Static Nonadaptive	Adaptive Deterministic Probabilistic	Fixed Variable Yes No	Request Request Assign work Request Chunk size	Global loop index
Static block cyclic (STATIC)				work size	Calculate chunk size
Self-scheduling (SS)					││
Fixed size chunking (FSC)					Chunk start
Guided self-scheduling (GSS)				Execute	fetch_and_add(Chunk size)
Factoring (FAC)				chunk	Execute chunk
Weighted factoring (WF)					

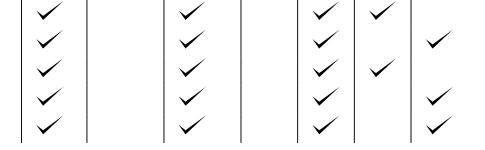
tational load per loop iteration, or irregular and unpredictable computing system characteristics

- Dynamic loop scheduling techniques are used to address load imbalance in computationallyintensive applications
- Perturbations and failures are expected to manifest increasingly in future HPC systems, with high count of processing elements (nodes, sockets, cores, ...)

#### Goal

Improve computationally-intensive scientific applications performance on HPC systems under unpredictable application and system characteristics via **robust scheduling** 

Adaptive weighted factoring (AWF-B) Adaptive weighted factoring (AWF-C) Adaptive weighted factoring (AWF-D) Adaptive weighted factoring (AWF-E) Adaptive factoring (AF)



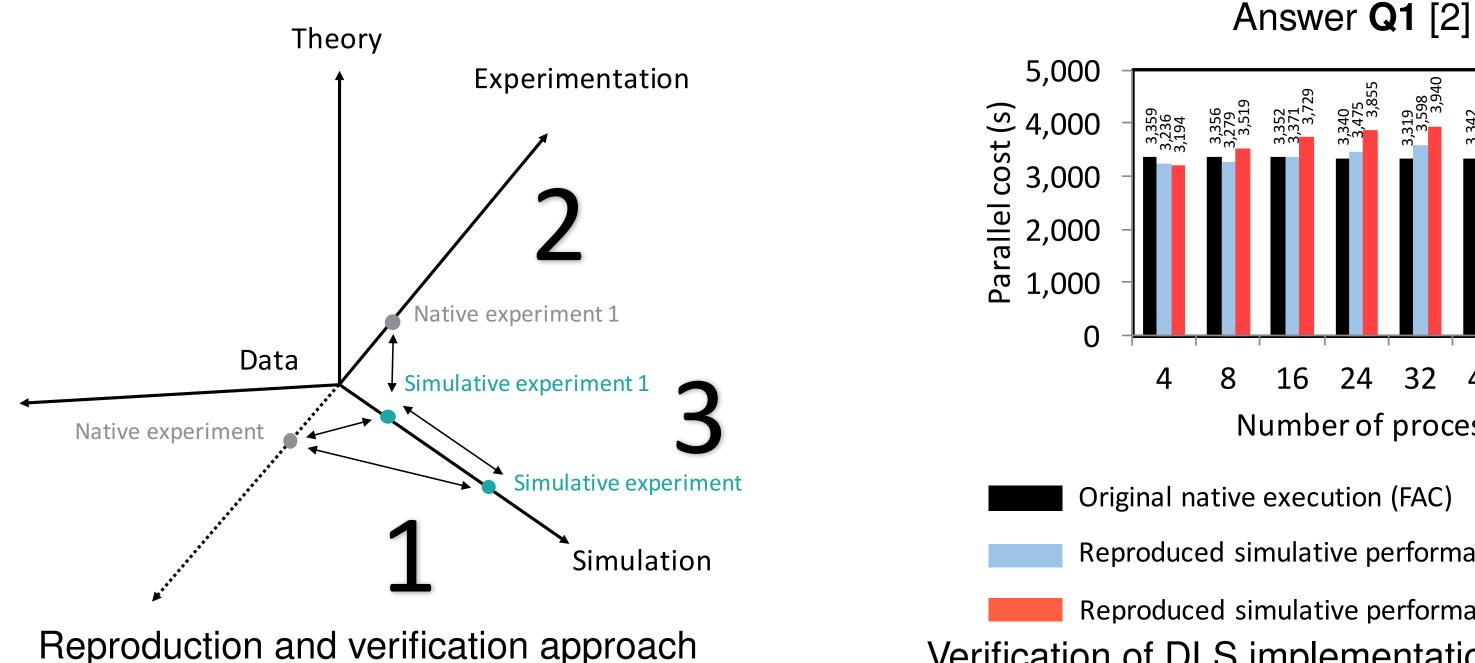
#### Worker thread i Thread i **Centralized control approach** Decentralized control approach

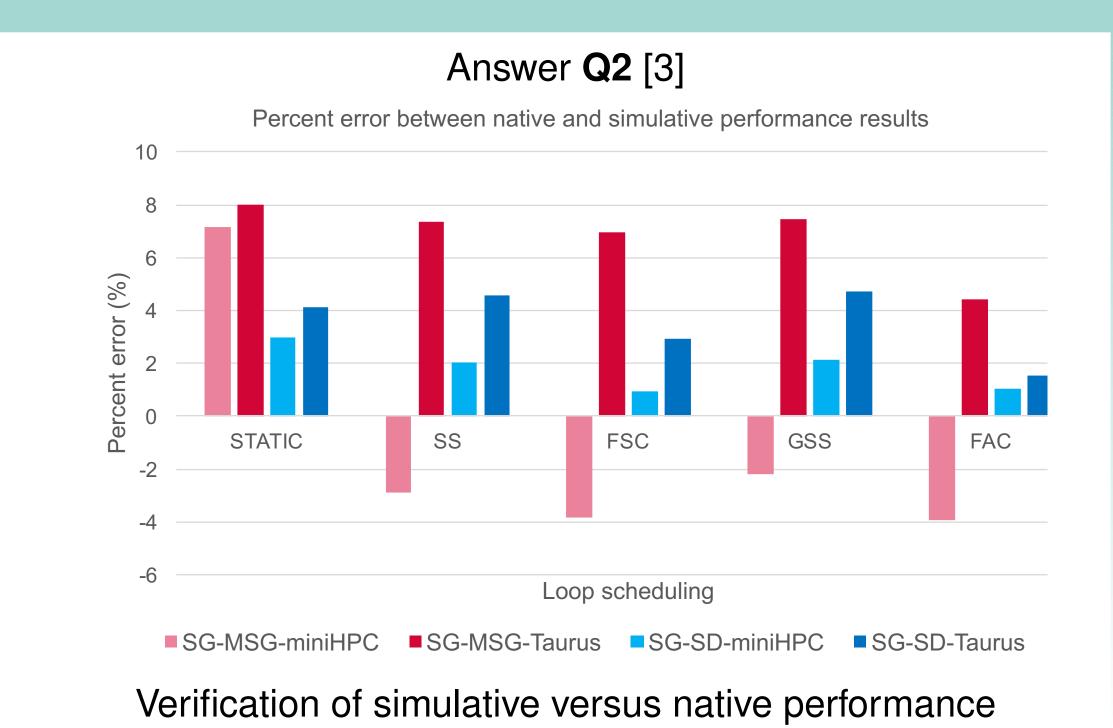
DLS implementation approaches

## **3. Research Questions**

- **Q1.** How *close* are the *implementations of DLS* techniques in simulation and native codes to their original proposed specifications decades ago?
- **Q2.** How *realistic* are the *simulations* of executions of scientific applications using DLS on HPC platforms?
- Q3. Given an application, a high-performance computing (HPC) system, and both their characteristics and interplay, which DLS technique will achieve improved performance under *unpredictable perturbations*?
- Q4. How to tolerate fail-stop *failures* of PEs during execution and maintain a balanced load enhanced application performance?
- **Q5.** How to *ensure* applications results *integrity* under silent data corruption (SDC) faults?

### 4. How Realistic are Simulations of Performance?





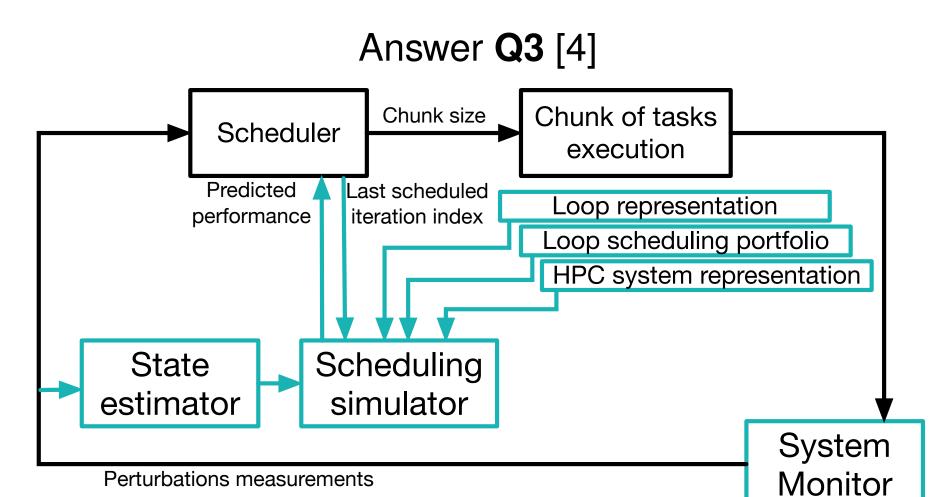
48 56 16 24 32 40 Number of processors

Original native execution (FAC)

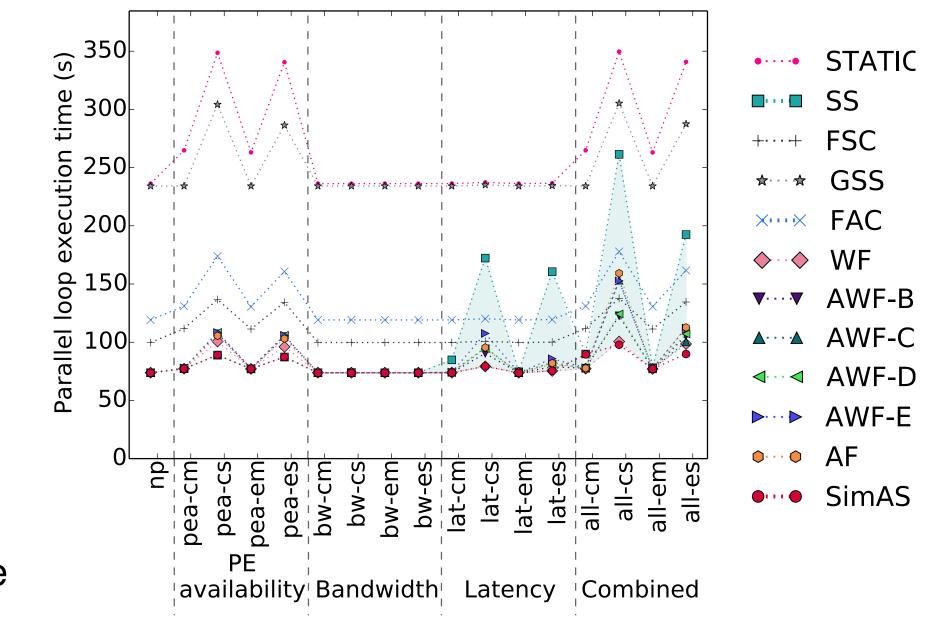
Reproduced simulative performance (centralized)

Reproduced simulative performance (decentralized) Verification of DLS implementation by reproduction

## 5. SimAS: Simulator Assisted Scheduling Under Perturbations



SimAS: Simulator assisted scheduling for DLS technique selection under perturbations



Performance under perturbations

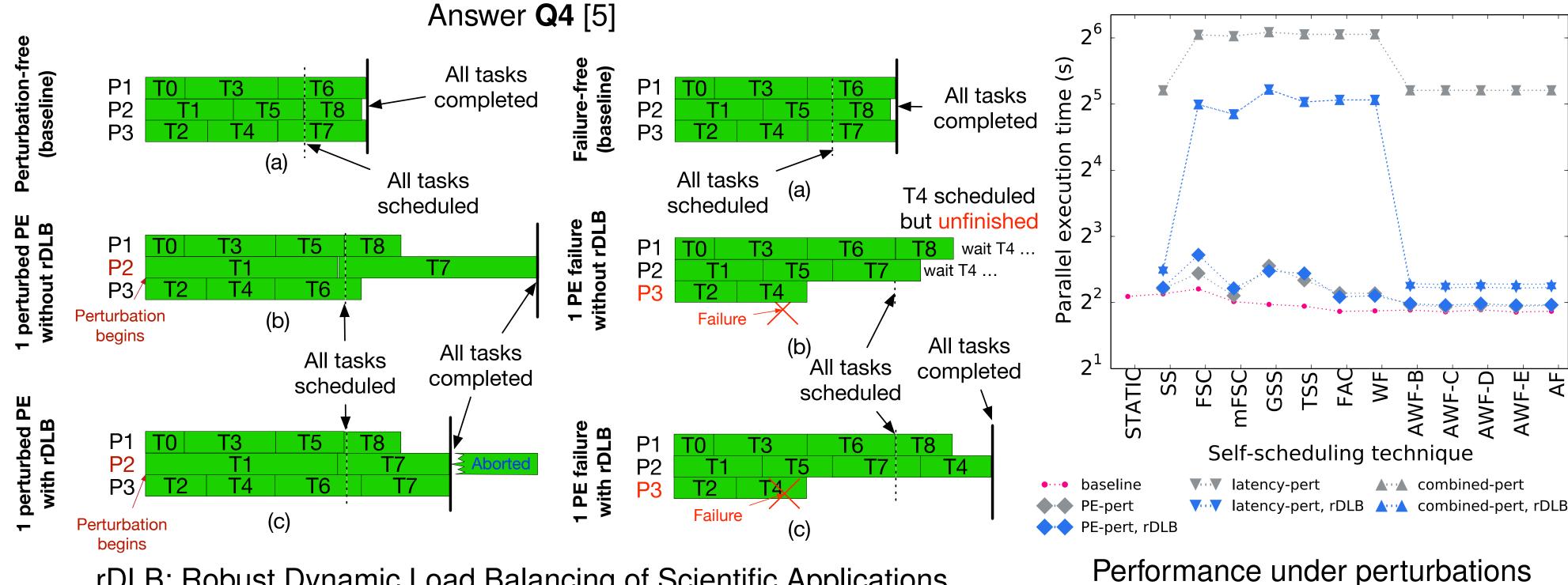
## 7. Next Steps

- Address SDC by replicating computations and solve scheduling challenges of replication (answer Q5)
- Analyze load imbalance in scientific applications at multiple levels of software parallelism
- Examining the performance of DLS at large scale via simulations
- Exploit multilevel scheduling to achieve fault tolerant application execution

## Acknowledgment

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## 6. rDLB: Robust Dynamic Load Balancing



rDLB: Robust Dynamic Load Balancing of Scientific Applications

## References

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