Power prediction with probabilistic topic modeling for HPC

Shigeto Suzuki¹, Michiko Hiraoka², Takashi Shiraishi¹, Hiroyuki Fukuda¹, Takuji Yamamoto¹, Shuji Matsui², Atsuya Uno³

¹FUJITSU LABORATORIES LTD, ²FUJITSU LTD, ³RIKEN Center for Computational Science

Abstract

Recently, HPC power prediction by machine learning using input job information has been studied. However, it requires time-consuming tuning of entry weights, because the each entry differently contributes to the power prediction.

In this paper, we propose a new two-step prediction scheme which contains "Topic model" and "Probabilistic model".

Without manual tuning of "weights" of entries, we successfully demonstrated the power prediction of K computer system with an average relative error of 18 %.

1. Introduction

- Towards the exascale supercomputer to be implemented in 2023, power saving operation is getting increasingly significant to reduce its operation cost.
- Power Capping, which controls system power not to exceed a preset power level, is known as a method for power saving operation. One form of adapting the power capping is a power-aware job scheduling method that predicts power of a job before its execution and controls the job execution order based on the predicted value.

Technique for predicting job power from job information can contribute to energy saving

- Power prediction techniques by machine learning using the entries in the submitted scripts, have been proposed.^[1] This approach requires manual tuning of "weight" for each entry, in order to make more effective entries contribute more to prediction. "Weights", however, could be different for each site.
- For example, site A has higher weights for user and group, while site B has a higher weight for job name in Fig.1.

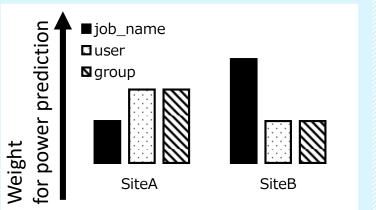
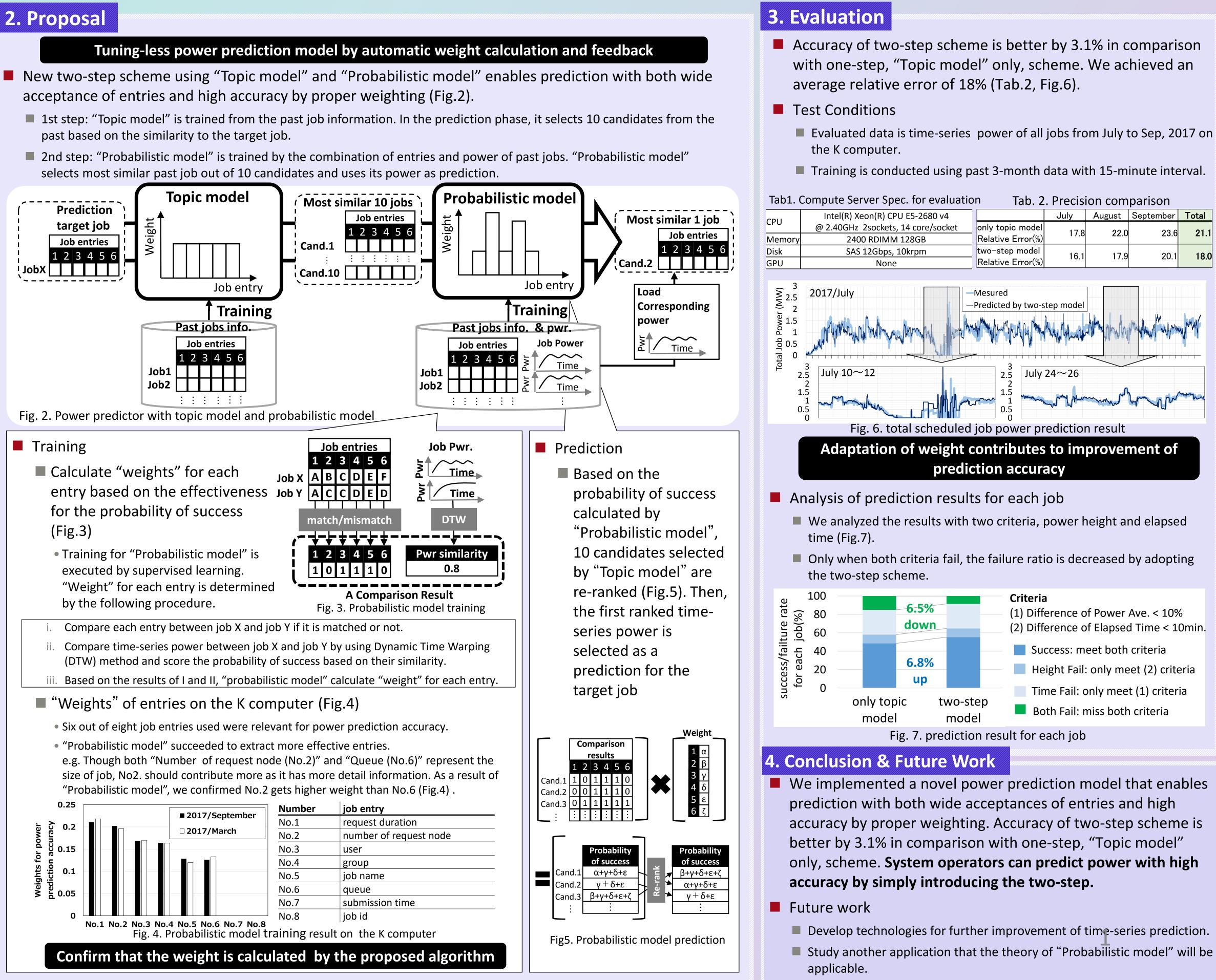


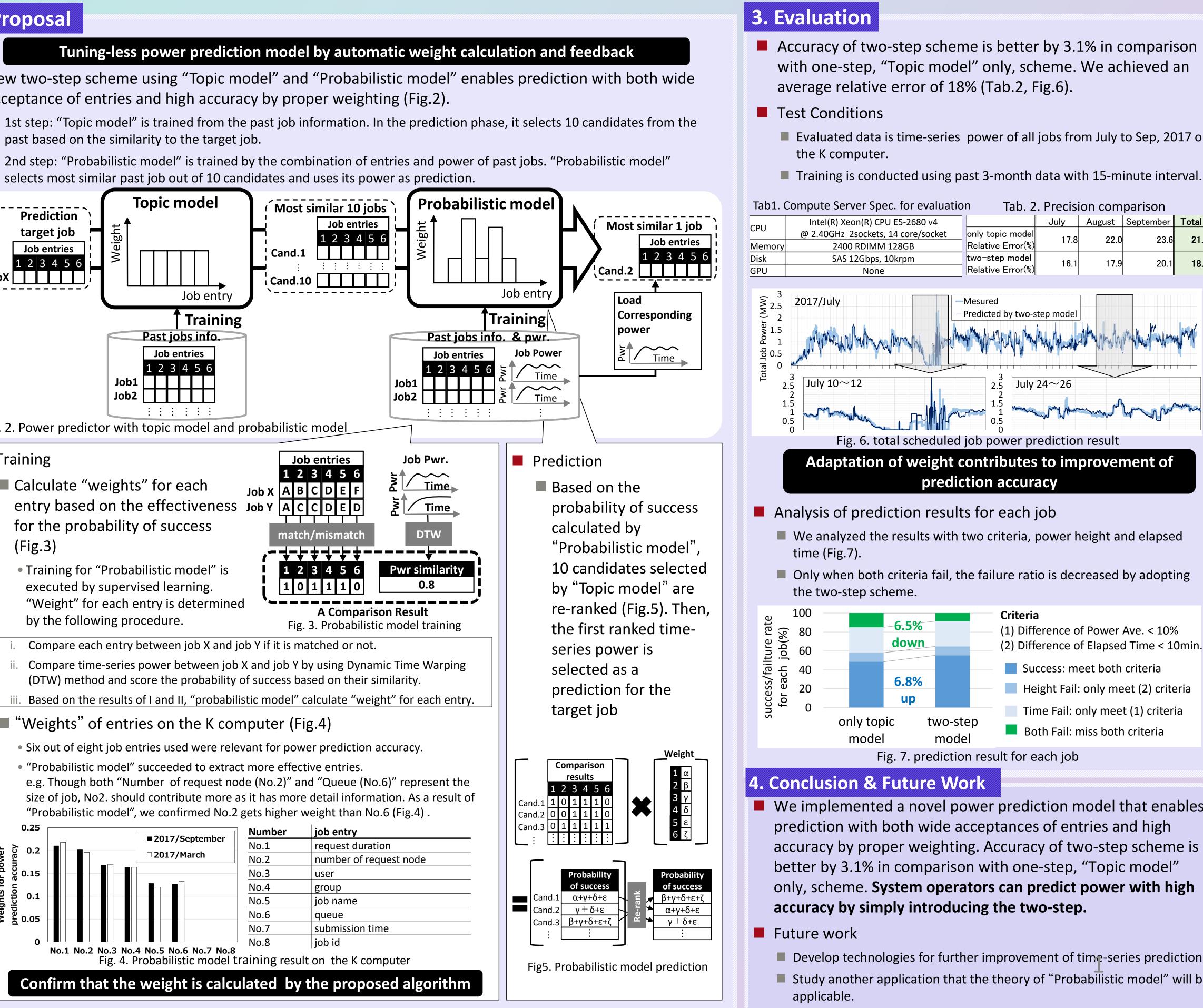
Fig. 1. Conceptual diagram of weight for power prediction at each site

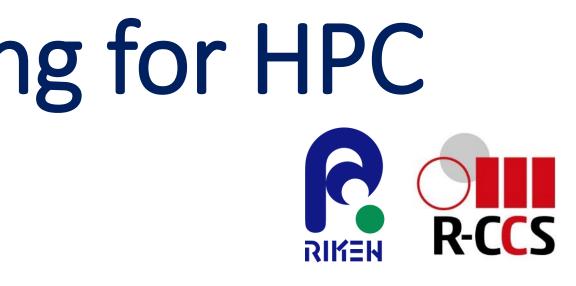
Challenge: Adaptive scheme to find optimal weights for each site

[1] Andrea Borghesi, Andrea Bartolini, Michele Lombardi, Michela Milano, Luca Benini, Scheduling-based Power Capping in High Performance Computing **Systems**, Sustainable Computing: Informatics and Systems 19, May 2018

2. Proposal







oute Server Spec. for evaluation Tab. 2. Precision comparison					
Intel(R) Xeon(R) CPU E5-2680 v4		July	August	September	Total
2.40GHz 2sockets, 14 core/socket	only topic model	17.8	22.0	23.6	21.1
2400 RDIMM 128GB	Relative Error(%)	17.0	22.0	23.0	21.1
SAS 12Gbps, 10krpm	two-step model	16.1	17.9	20.1	18.0
None	Relative Error(%)				