# Research Software Engineering enabling HPC

#### Anna Brown



#### Why Research Software Engineering?

- Increasingly complex HPC architectures require dedicated expertise
- Increasingly complex codes require good software practice and time dedicated to maintenance
- An RSE combines SE expertise with an understanding of research to develop software in close collaboration with researchers

# Join the community at RSE2019



#### Plasma physics simulation

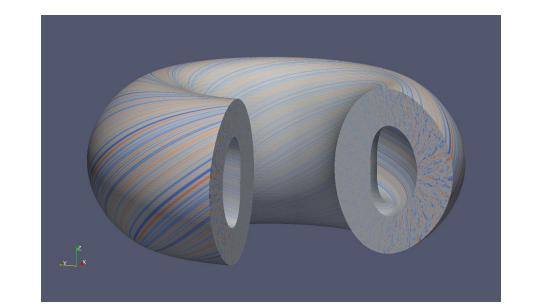


QuEST

#### Quantum computing simulation



GS2 is an open source Fortran 95 code for simulating turbulence in magnetized plasma on CPU, parallelised with MPI and scaling up to O(10k) cores.



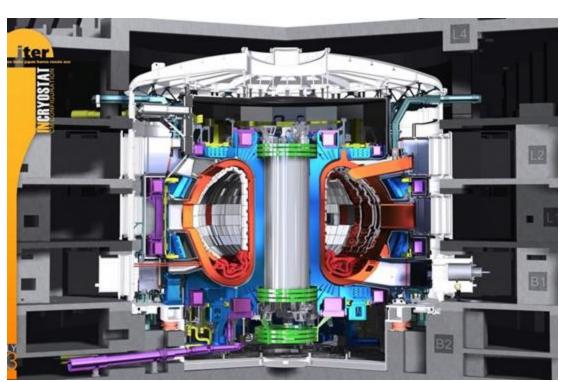


QuEST is an open source library for simulating quantum computing on classical computers, written in C and parallelised with OpenMP across a single CPU node, MPI across multiple nodes and CUDA on GPU.



#### Why HPC?

- The experimental fusion reactor ITER will have 10 times the plasma volume of the largest device currently in operation.
- Need to dramatically increase the performance of the existing GS2 code to model science at this scale.



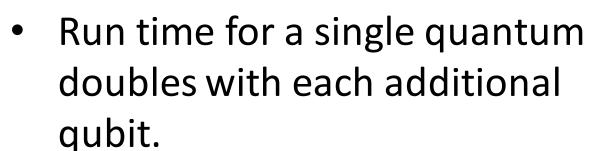
**ITER** 

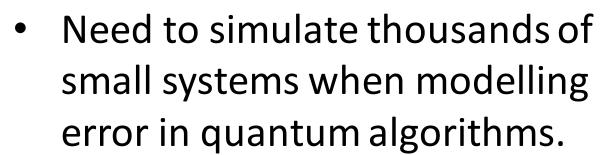
## Optimising unavoidable MPI communication

- GS2 uses a distributed 7 dimensional array.
- Each timestep contains reduction steps across several dimensions, always hitting some data that is not local to a process
- Communication will always be the bottleneck here; needs to be efficient
- We used profiling tools to visualise communication patterns and bottlenecks – understood load imbalance, limiting factor

#### Why HPC?

- Memory requirements double with each additional qubit.
- Need to simulate large systems to verify real quantum computers are working correctly
- To simulate just 40 qubits takes 32 TB of RAM.

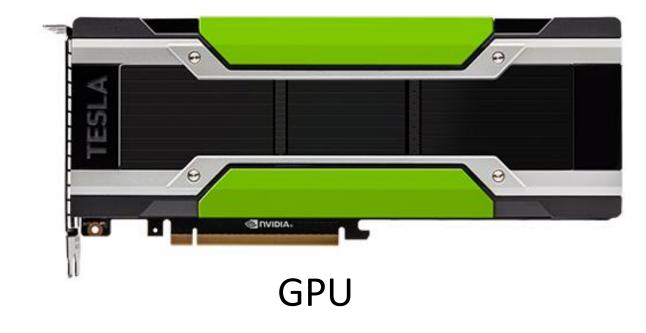




Each individual simulation needs to run fast.

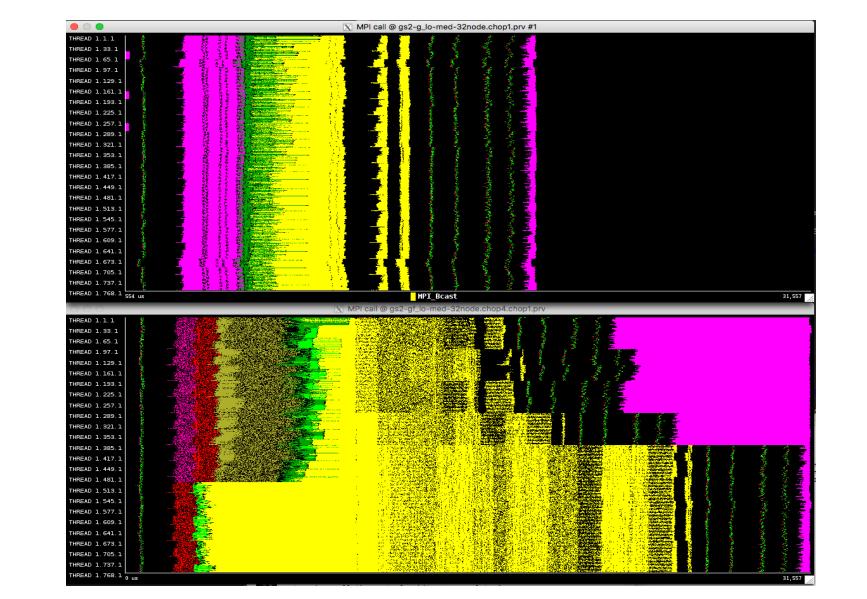


Archer national supercomputer



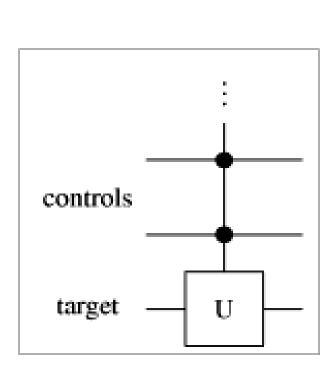
### **Profiling at scale**

- Many communication bottlenecks only become apparent at scale
- Profiling at scale is complicated
- Special techniques, eg filtering large visualisation files and optimising parallel I/O.



Hiding HPC complexity from users

- Users need access to HPC without detailed knowledge of the architecture.
- QuEST uses the same API for circuits running on single CPU, distributed across multiple CPUs and on GPU
- Users can easily scale up code developed on a laptop.
- Needs learnt through close collaboration with domain experts



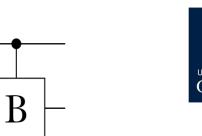
#### Avoiding global communication

- The critical bottleneck was found to be in the duplication of a distributed data structure onto a single process for verification purposes.
- Little impact at small process counts
- A typical use case is now at the scale where this global communication has a significant impact.
- Initial tests on ~3k cores suggest that removing this bottleneck could lead to a run time reduction of 20%.

# **Collaborators**

Simon Benjamin, Tyson Jones





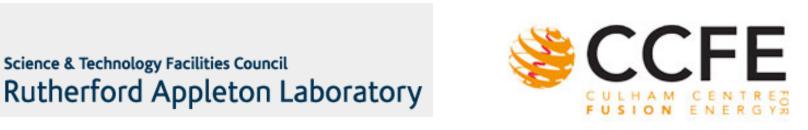
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#### Collaborators

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#### Our team



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