

# ISC High Performance

The HPC Event.

SUNDAY, JUNE 24 –  
THURSDAY, JUNE 28, 2018  
FRANKFURT, GERMANY



CONFERENCE GUIDE

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## Welcome to ISC High Performance 2018

Today ISC High Performance stands proudly as a globally recognized HPC event, driven by the extensive involvement of the high performance computing community. The contribution of new topics, speakers, stringent review processes, as well as diversity efforts, makes ISC distinctive.

There are a number of key facets to this distinctiveness. First, we are a truly international forum; over 70 percent of our attendees and exhibitors are from outside Germany, representing about 65 countries. This geographic diversity is apparent in our list of speakers, committees and the exhibition. Another unique aspect of the conference is the balance between academia and industry attendees, with 59 and 41 percent representation, respectively. Addressing their individual challenges and needs, we offer full-day sessions, such as the Industrial Day and Machine Learning Day.

Furthermore, the 2018 Program Chair, Horst Simon, has introduced topics like climate change and cosmology, to spur interesting conversations between researchers and vendors at the conference. He also ensured that this year's keynotes are about projects with global impacts. On Monday, Dr. Maria Girone will speak about the computing challenges of the Large Hadron Collider at CERN, while Dr. Keren Bergman will speak on Tuesday about research in optical interconnection networks for advanced computing systems.

This year we are also hosting the largest ISC exhibition, with 162 exhibitors. Take the time to meet the vendors and research organizations, whose products and technologies help advance science and engineering.

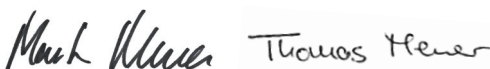
In recognizing underappreciated talent and to help alleviate the gender gap, we introduced a gender diversity strategy in 2017. As a result, this year almost 40 percent of our distinguished and keynote speakers are women. Even our student volunteer program received a record-breaking number of applications from female university students.

The ISC STEM Program returns to the benefit of 200 university students, where we provide them free access to the conference, a custom-made tutorial and career advice. This year we also set up the ISC Travel Grant to enable a student and a young researcher, who otherwise lack funding, to join us at the conference.

Here we thank everyone involved for making ISC 2018 a great conference.

Finally, we have added some football flavor to the event and hope you'll enjoy it. Let's celebrate HPC and the World Cup at ISC 2018.

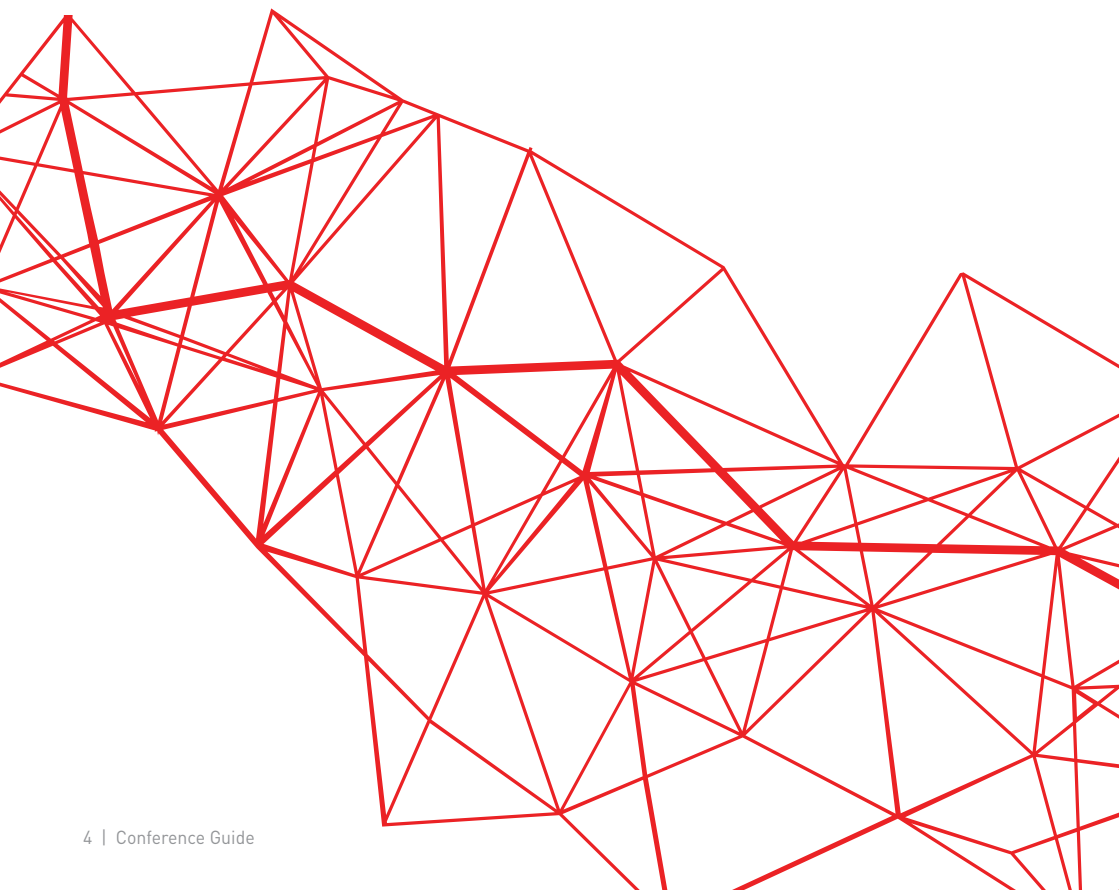
Best regards,



Martin Meuer and Thomas Meuer

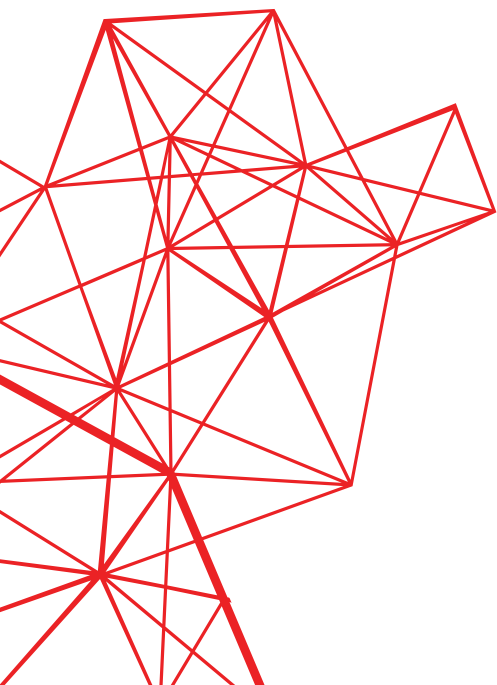


Thomas and Martin Meuer



**General  
Information**

**ISC 2018**



**ACCESSIBILITY**

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To help you navigate the ISC Conference, please consult Messe Frankfurt’s information on a barrier-free visit.

**AGENDA**

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An updated version of the conference program can be found on our website. The ISC Agenda lets you navigate easily through the conference program and provides information on sessions and speakers. With a few clicks, you can design your own schedule. Please visit <https://2018.isc-program.com/>.

**CHECK-IN & SELF REGISTRATION COUNTER**

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The Check-In and Self Registration Counter can be found in the Foyer of Hall 3.

**Opening Hours**

Sunday, June 24:	07:30 am - 06:00 pm
Monday, June 25:	07:30 am - 06:00 pm
Tuesday, June 26:	07:30 am - 06:00 pm
Wednesday, June 27:	07:30 am - 04:00 pm
Thursday, June 28 (Marriott Hotel):	07:30 am - 02:00 pm

**CLOAKROOM**

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A cloakroom is available in the Foyer of Hall 3.

**Opening Hours**

Sunday, June 24:	07:30 am - 07:00 pm
Monday, June 25:	07:30 am - 09:00 pm
Tuesday, June 26:	07:30 am - 07:00 pm
Wednesday, June 27:	07:30 am - 07:00 pm

COPY & PRINT CENTER

Irmschler Repro GmbH provides copy and printing services at booth #N-100 in the Exhibition Hall (Hall 3.0).

Opening Hours

Saturday, June 23:	01:00 pm – 06:00 pm
Sunday, June 24:	09:00 am – 06:00 pm
Monday, June 25:	09:00 am – 06:00 pm
Tuesday, June 26:	09:00 am – 06:00 pm
Wednesday, June 27:	09:00 am – 01:00 pm

During these hours, orders can be placed and picked up at the service booth #N-100. Orders placed before Saturday, June 23, will be ready for pick-up on-site on Saturday afternoon.

Ordering and Questions:

Tel: +49 6103 930140  
Email: [mi@irmschler.eu](mailto:mi@irmschler.eu) (Mr. Michael Irmschler)  
Website: <https://ir-repro.de/> (German only)

EXHIBITION

The ISC High Performance exhibition features the largest collection of HPC vendors, universities, and research organizations assembled annually in Europe. Detailed information regarding the exhibition is available at <http://isc-hpc.com/overview-sponsors-exhibitors.html> and in the mobile app “ISC 2018 Exhibition”.

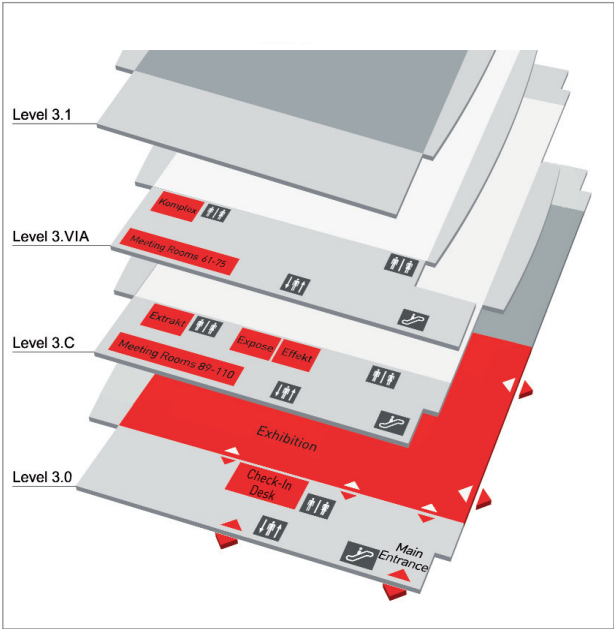
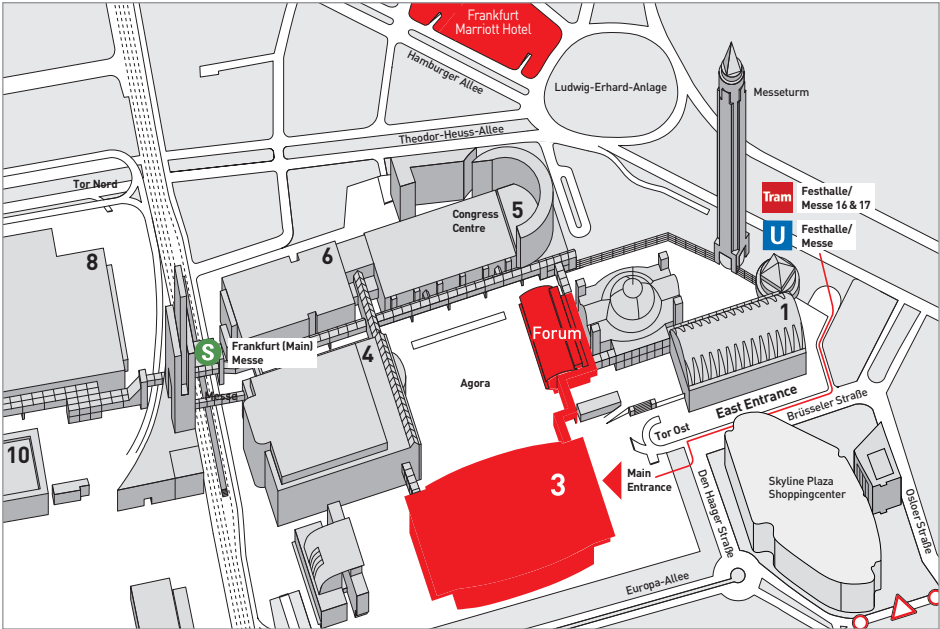
Exhibition Hours

Monday, June 25:	03:00 pm – 08:30 pm
	(ISC Welcome Party 06:30 pm – 08:30 pm)
Tuesday, June 26:	10:00 am – 06:00 pm
Wednesday, June 27:	10:00 am – 06:00 pm

For the exhibition floor plan and the exhibitor listing, please refer to the Pocket Guide. The Pocket Guide is available at the Check-In Counter (Foyer, Hall 3) and the Information Counter (Foyer, Hall 3).

EXHIBITION LOUNGES

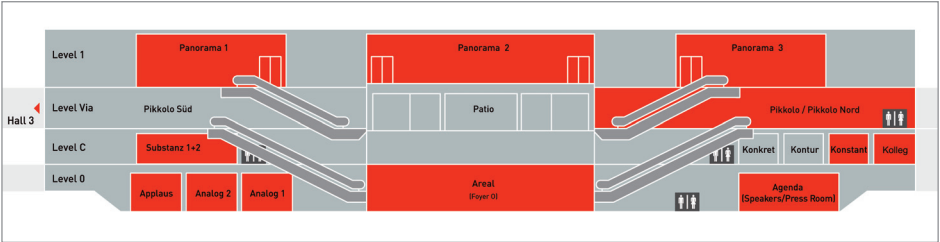
There are two lounges located inside the Exhibition Hall (Hall 3.0): The TOP500 Lounge (#N-222) and another cozy seating area at #L-420. Both lounges feature device charging stations.



Overview of Messe Frankfurt (Hall 3, Forum) and Frankfurt Marriott Hotel

Hall 3

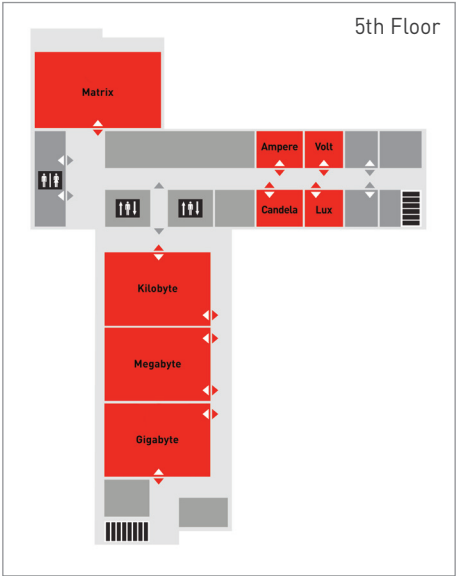




Forum



Workshop Rooms  
at the  
Frankfurt Marriott Hotel



EXHIBITOR SERVICES HELP DESK

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If you have any exhibition-related questions or need technical assistance on-site, please contact us at booth #N-100 in the Exhibition Hall.

Service Hours

June 23 - June 27: ..... 09:00 am – 07:00 pm

If support is needed outside of these dates and times, please contact us via email: exhibitor-support@isc-events.com

FIRST AID

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In the event of a medical emergency, please contact the Check-In Counter (Foyer, Hall 3), Information Counter (Foyer) or the Exhibitor Services helpdesk in the Exhibition Hall (Hall 3). If you are unable to locate these, please call the emergency number 112 or the Frankfurt Messe Operation and Security Center Hotline + 49 (0)69 7575 3333.

FLOOR & EXHIBITION PLANS

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Please refer to the Pocket Guide for floor and exhibition plans. The Pocket Guide is available at the Check-In Counter (Foyer, Hall 3) and the Information Counter (Foyer, Hall 3).



FREE PUBLICATIONS

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Free HPC and IT magazines are available at the Check-In Counter (Foyer), in the Exhibition Hall (Hall 3) and the Forum Foyer (Level 0).

INFORMATION COUNTER

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The ISC Information Counter is located in the Foyer of Hall 3.

Opening Hours

Sunday, June 24: ..... 07:30 am – 05.00 pm  
Monday, June 25: ..... 07:30 am – 07:30 pm  
Tuesday, June 26: ..... 07:30 am – 05:00 pm  
Wednesday, June 27: ..... 07:30 am – 05:00 pm

ISC CODE OF CONDUCT

ISC High Performance is dedicated to providing a harassment-free conference experience for everyone, regardless of gender, gender identity and expression, age, sexual orientation, disability, physical appearance, body size, ethnicity, religion (or lack thereof), manner of articulation, or technology choices. We do not tolerate harassment of participants in any form.

As a professional event that aims to bring together HPC enthusiasts from various countries to actively share knowledge, ideas and network, we recognize the inherent worth of every person by being respectful to them. All forms of communication should be appropriate for a professional audience including people of different backgrounds.

Participants violating the ISC High Performance Code of Conduct may be asked to leave the conference without a refund at the sole discretion of the conference organizers and also banned from future conferences.

Please contact us at [conduct@isc-events.com](mailto:conduct@isc-events.com) if you have questions or experience any violation of our code of conduct.

ISC ORIENTATION SESSION

The ISC Orientation Session is intended as an introductory session for ISC High Performance attendees who want to learn to navigate through the conference, make the best use of their time at the event, and have fun in Frankfurt. We hope the information and tips we share with you at this session will make ISC 2018 a great experience for you.

Where: Room Konstant, Forum  
When: Monday, June 25, 01:00 pm - 01:30 pm

ISC STEM STUDENT DAY & GALA

ISC High Performance is a forum for HPC community members to network and explore opportunities - for both current experts and future generations. We have created a program to welcome science, technology, engineering, and mathematics (STEM) students into the world of HPC, demonstrate how technical skills can propel their future careers, introduce them to the current job landscape, and also show them what the HPC workforce will look like in 2020 and beyond.

Where: Room Konstant (Forum), Hall 3 & Marriott Frankfurt  
When: Wednesday, June 27, 09:30 am - 09:30 pm

Admission is free for undergraduate and graduate students, however is limited to 200 attendees who signed up in advance.

### ISC WELCOME PARTY

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The ISC Welcome Party is famous for its food, drinks, entertainment and amiable atmosphere. It gives you the opportunity to visit vendors and service providers of your choice, and also socialize with other HPC enthusiasts. An entertaining party duo - consisting of a DJane and a saxophonist - will be on the exhibition floor this year, to add swing and groove to the exhibition opening. We look forward to seeing you there!

Where: Exhibition Hall (Hall 3)

When: Monday, June 25, 06:30 pm – 08:30 pm



### LOST BADGE FEE

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If you lose your conference badge, please contact the Check-In Counter (Foyer, Hall 3). Please note there is a processing fee of € 30 to replace lost badges.

### MOBILE APP - ISC 2018 EXHIBITION

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In cooperation with the Poznan Supercomputing & Networking Center (PSNC), ISC offers the mobile app **"ISC 2018 Exhibition"**. This app is available in Google Play, App Store and Windows Phones stores free of charge to ISC 2018 attendees. If you have PSNC's "Conference4me" app installed on your device, you can also access all ISC 2018 exhibition information via this mobile conference assistant.

The logo for ISC 2018, featuring the text "ISC" stacked above "2018" in white, set against a solid red square background.

### NETWORK HELP DESK

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WiFi is available throughout the venue, login data will be printed on your badge. If you need assistance or have any questions regarding internet connectivity, please contact [iscnet@isc-events.com](mailto:iscnet@isc-events.com) or Exhibitor Support at booth #N-100.

Please respect our WiFi Policy. ISC 2018 exhibitors and attendees are not allowed to operate any IEEE 802.11 wireless access point or any other equipment transmitting in the 2.4 GHz or 5.2 GHz frequency range in the conference and exhibition venue during ISC 2018. This also includes build-up hours.

The ISC 2018 organizers reserve the right to impose a fine on anyone who violates this policy.

## PASSES OVERVIEW

	Tutorial Pass	Conference Pass	Exhibition Pass	Workshop Pass
Tutorials	✓			
Focus Sessions		✓		
Keynotes		✓		
Distinguished Speakers		✓		
Panels		✓		
Industrial Day		✓		
Machine Learning Day		✓		
HPC in Asia		✓		
Research Papers		✓		
Research Posters		✓		
PhD Forum		✓	✓	
Project Posters		✓	✓	
BoFs		✓	✓	
Vendor Showdown		✓	✓	
Exhibitor Forum		✓	✓	
HPCAC-ISC Student Cluster Competition Award Ceremony		✓	✓	
Exhibition		✓	✓	
Outreach & Networking Programs		✓	✓	
Workshops				✓

**PARKING**

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To access the fair grounds with a vehicle please use Tor Nord (Gate North). The designated parking lot for the duration of the event (June 24-27) is P3. Parking permits can be obtained on site. Please note: vehicles seeking access to the fairgrounds between Monday, June 25, 03:00 pm and Wednesday, June 27, 02:00 pm, will have to pay a € 100 deposit, which will only be returned if the vehicle leaves the premises no later than one (1) hour after entry. This does not apply to small vehicles with a parking permit.

**PRESS ROOM**

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A combined speakers and press room is located in the Forum building on Level 0 in room “Agenda”.

**Opening Hours**

Sunday, June 24:	07:30 am – 07:00 pm
Monday, June 25:	07:30 am – 07:00 pm
Tuesday, June 26:	07:30 am – 07:00 pm
Wednesday, June 27:	07:30 am – 07:00 pm

**PROCEEDINGS**

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The conference proceedings will be available online with presentations provided as PDF files a week after the event. The ISC 2018 attendees will receive an email with the access link. All accepted research papers will also be published in the Springer’s Lecture Notes in Computer Science series (LNCS 10876), and the papers can be downloaded from Springer’s website for a limited time after ISC 2018.

**SEATING AREAS**

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There are seating areas in Hall 3, both on Level 0 (main entrance) and on Level Via (near Via Mobile). These areas feature power outlets to charge your devices.

SPEAKERS ROOM

A combined speakers and press room is located in the Forum building on Level 0 in room “Agenda”.

Opening Hours

Sunday, June 24:	07:30 am – 07:00 pm
Monday, June 25:	07:30 am – 07:00 pm
Tuesday, June 26:	07:30 am – 07:00 pm
Wednesday, June 27:	07:30 am – 07:00 pm

WORKSHOPS

The workshops will be held on June 28 at the Frankfurt Marriott Hotel, located on Hamburger Allee across from the Congress Center. If you are interested in attending the workshops but have not registered yet, you can do so at the Self Registration Counter, located in the Foyer of Hall 3 or at Marriott Hotel on June 28 from 07:30 am until 02:00 pm.

# TABOR COMMUNICATIONS



## The Information Nexus of Advanced Computing and Data Systems for a High-performance World

Tabor Communications is an international media, services & events company dedicated to fostering community and accelerating adoption of advanced-scale computing technologies.

### SERVICES PROVIDED

- Display Advertising
- Front Page Takeovers
- Dedicated Eblasts
- Virtual Booth Tour Videos
- Technology Product Showcases

- Custom Whitepapers
- Webinars
- Job Recruitment
- Advertorials
- Solution Channel Microsites

### TABOR EVENTS

Join us this coming year for the HPC on Wall Street conference and Advanced Scale Forum. HPC on Wall Street, a newly refreshed innovation event dedicated to bringing together industry forerunners that are setting the standards for Capital Markets today, will take place in New York's Roosevelt Hotel on September 13, 2018. Advanced Scale Forum, slated for May 2019, connects leading IT solution providers with the industry trailblazers that depend on big data and big compute for a competitive edge.



[taborcommunications.com](http://taborcommunications.com) | 858-625-0070

General Information

Sunday, June 24

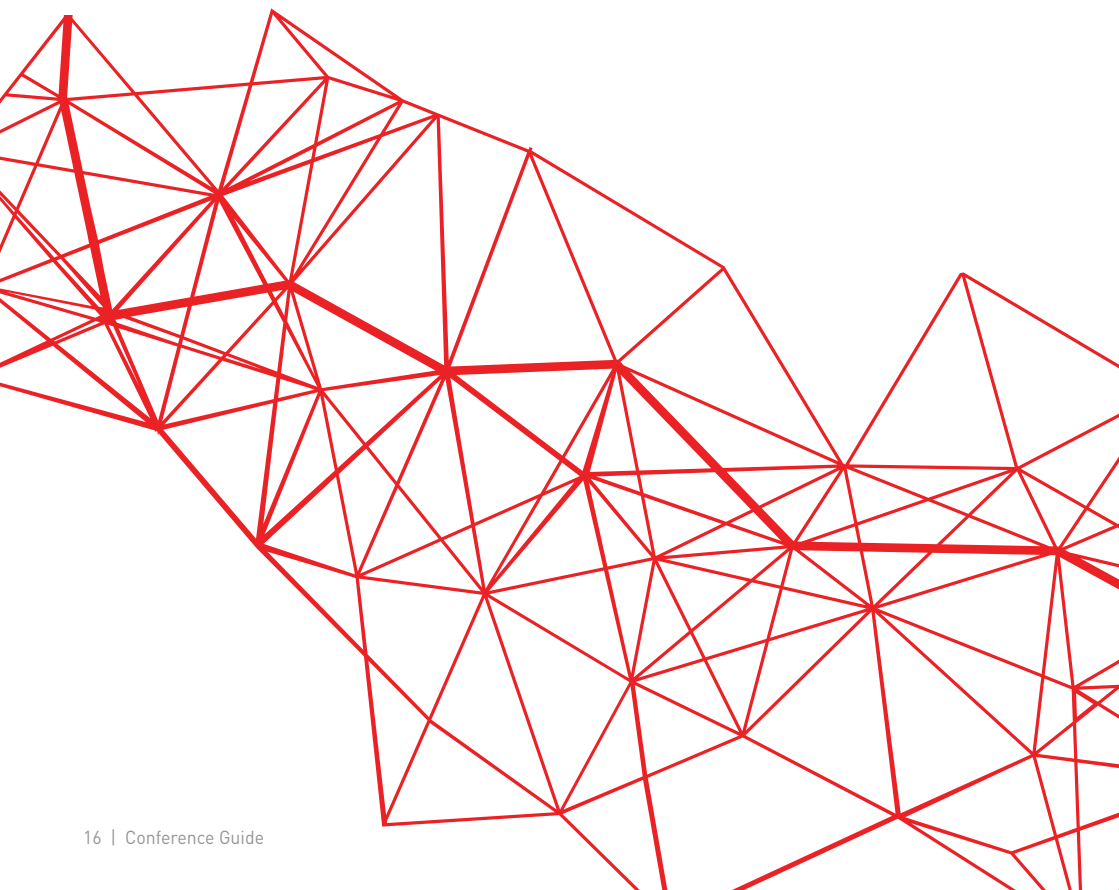
Monday, June 25

Tuesday, June 26

Wednesday, June 27

Thursday, June 28

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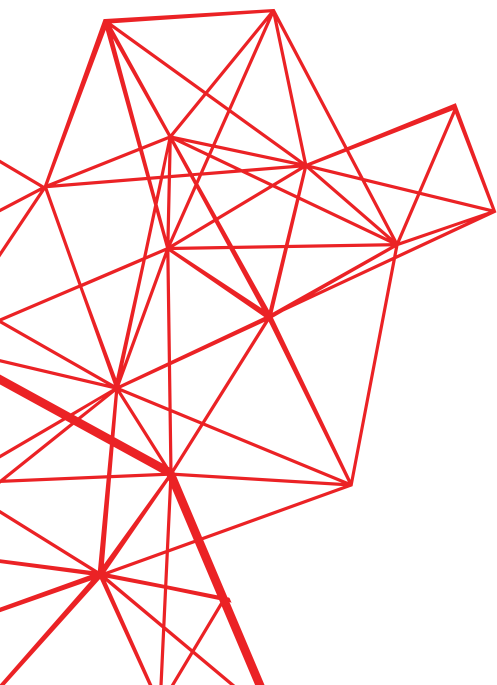


# PROGRAM

## Tutorials

(in chronological order per room)

**SUNDAY,  
JUNE 24**



ROOM	8am	9am	10am	11am	12pm	1pm	2pm	3pm	4pm	5pm	6pm	7pm	8pm	9pm
Analog 1			Mastering Tasking with OpenMP					Advanced OpenMP: Performance and 4.5 Features						
Analog 2			Introduction to Parallel I/O					A Practical Approach to Application Performance tuning with the Roofline Model						
Applaus							Advanced MPI							
Substanz 1 + 2							Deep Learning Demystified							
Konstant			Getting Started with Containers on HPC through Singularity					Better Scientific Software						
Kolleg							Hands-on Practical Hybrid Parallel Application Performance Engineering							
Effekt			InfiniBand, Omni-Path, and High-Speed Ethernet for Beginners					InfiniBand, Omni-Path, and High-Speed Ethernet: Advanced Features, Challenges in Designing HEC Systems and Usage						
Expose			Boosting Power Efficiency of HPC Applications with GEQPM					Using the SPEC HPC Benchmarks for Better Analysis and Evaluation of Current and Future HPC Systems						
Extrakt							Accelerating Applications with Massively Parallel Computing							
■ CONFERENCE PASS ■ EXHIBITION PASS ■ TUTORIAL PASS ■ WORKSHOP PASS														
Program may be subject to changes; the latest version of the program is available at <a href="https://2018.isc-program.com/">https://2018.isc-program.com/</a>														

Analog 1		
09:00 am - 01:00 pm	<b>■ Mastering Tasking with OpenMP</b> <i>Presenters:</i> <i>Christian Terboven, RWTH Aachen University</i> <i>Michael Klemm, Intel</i> <i>Sergi Mateo Bellido, Barcelona Supercomputing Center</i> <i>Xavier Teruel, Barcelona Supercomputing Center</i> <i>Bronis R. de Supinski, Lawrence Livermore National Laboratory</i>	Analog 1
02:00 pm - 06:00 pm	<b>■ Advanced OpenMP: Performance and 4.5 Features</b> <i>Presenters:</i> <i>Christian Terboven, RWTH Aachen University</i> <i>Michael Klemm, Intel</i> <i>James C. Beyer, NVIDIA</i> <i>Kelvin Li, IBM</i> <i>Bronis R. de Supinski, Lawrence Livermore National Laboratory</i>	Analog 1
Analog 2		
09:00 am - 01:00 pm	<b>■ Introduction to Parallel I/O</b> <i>Presenters:</i> <i>Ritu Arora, Texas Advanced Computing Center</i> <i>John Cazes, Texas Advanced Computing Center</i> <i>Robert McClay, Texas Advanced Computing Center</i>	Analog 2
02:00 pm - 06:00 pm	<b>■ A Practical Approach to Application Performance tuning with the Roofline Model</b> <i>Presenters:</i> <i>Tuomas S. Koskela, LBNL</i> <i>Aleksandar Ilic, INESC-ID, Instituto Superior Técnico, Universidade de Lisboa</i> <i>Zakhar A. Matveev, Intel</i> <i>Roman Belenov, Intel</i> <i>Charlene J. Yang, LBNL</i> <i>Leonel Sousa, INESC-ID, Instituto Superior Técnico, Universidade de Lisboa</i>	Analog 2

General Information

Sunday, June 24

Monday, June 25

Tuesday, June 26

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Thursday, June 28

### Applaus

09:00 am - 06:00 pm	<b>■ Advanced MPI</b> <i>Presenters:</i> <i>Pavan Balaji, Argonne National Laboratory</i> <i>Torsten Hoefer, ETH Zurich</i>	Applaus
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### Kolleg

09:00 am - 06:00 pm	<b>■ Hands-on Practical Hybrid Parallel Application Performance Engineering</b> <i>Presenters:</i> <i>Christian Feld, Jülich Supercomputing Centre (JSC)</i> <i>Markus Geimer, Jülich Supercomputing Centre (JSC)</i> <i>Sameer Shende, University of Oregon; ParaTools, Inc.</i> <i>Ronny Tschüter, Technische Universität Dresden</i>	Kolleg
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### Konstant

09:00 am - 01:00 pm	<b>■ Getting Started with Containers on HPC through Singularity</b> <i>Presenters:</i> <i>Andrew Younge, Sandia National Laboratories</i> <i>Carlos Eduardo Arango, Sylabs</i>	Konstant
02:00 pm - 06:00 pm	<b>■ Better Scientific Software</b> <i>Presenters:</i> <i>Anshu Dubey, Argonne National Laboratory, University of Chicago</i> <i>Michael Heroux, Sandia National Laboratories, St. Johns University</i>	Konstant

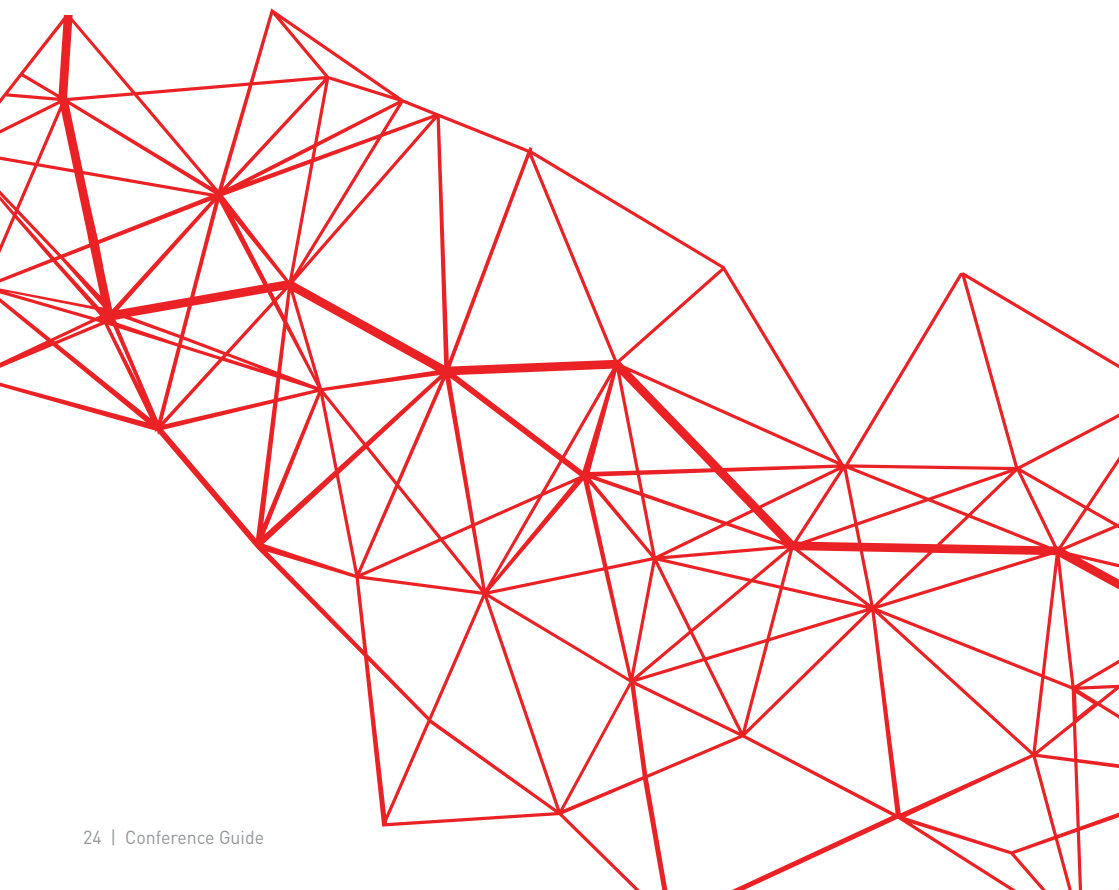
### Substanz 1 + 2

09:00 am - 06:00 pm	<b>■ Deep Learning Demystified</b> <i>Presenter:</i> <i>Yu Wang, Leibniz Supercomputing Center</i>	Substanz 1 + 2
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Effekt			General Information
09:00 am - 01:00 pm	<b>■ InfiniBand, Omni-Path, and High-Speed Ethernet for Beginners</b> <i>Presenters:</i> <i>Dhabaleswar Panda, The Ohio State University</i> <i>Hari Subramoni, The Ohio State University</i>	Effekt	
02:00 pm - 06:00 pm	<b>■ InfiniBand, Omni-Path, and High-Speed Ethernet: Advanced Features, Challenges in Designing HEC Systems and Usage</b> <i>Presenters:</i> <i>Dhabaleswar Panda, The Ohio State University</i> <i>Hari Subramoni, The Ohio State University</i>	Effekt	Sunday, June 24
Expose			Monday, June 25
09:00 am - 01:00 pm	<b>■ Boosting Power Efficiency of HPC Applications with GEOPM</b> <i>Presenters:</i> <i>Tapasya Patki, Lawrence Livermore National Laboratory</i> <i>Martin Schulz, Technical University of Munich</i> <i>Jonathan M. Eastep, Intel</i>	Expose	
02:00 pm - 06:00 pm	<b>■ Using the SPEC HPG Benchmarks for Better Analysis and Evaluation of Current and Future HPC Systems</b> <i>Presenters:</i> <i>Robert Henschel, Indiana University</i> <i>Sandra Wienne, RWTH Aachen University</i> <i>Bo Wang, RWTH Aachen University</i>	Expose	Tuesday, June 26
Extrakt			Wednesday, June 27
09:00 am - 06:00 pm	<b>■ Accelerating Applications with Massively Parallel Computing</b> <i>Presenter:</i> <i>Wen-Mei Hwu, University of Illinois Urbana/Champaign</i>	Extrakt	
			Thursday, June 28

Coffee & Lunch Breaks		
08:00 am - 09:00 am	Welcome Coffee	Tutorial Rooms
11:00 am - 11:30 am	Coffee Break	Forum, Level Via: Patio, Pikkolo Süd, Pikkolo Nord
01:00 pm - 02:00 pm	Lunch Break	Forum, Level Via: Patio, Pikkolo Süd, Pikkolo Nord
04:00 pm - 04:30 pm	Coffee Break	Forum, Level Via: Patio, Pikkolo Süd, Pikkolo Nord

## NOTES



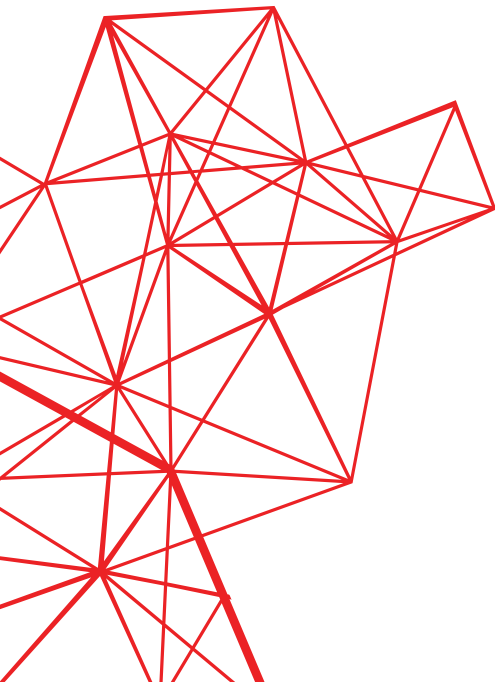


# PROGRAM

Conference

(in chronological order per room)

**MONDAY,  
JUNE 25**



ROOM	8am	9am	10am	11am	12pm	1pm	2pm	3pm	4pm	5pm	6pm	7pm	8pm	9pm
Analog 1 + 2							PhD Forum							
Konstant						ISC Orientation Session								
Panorama 1									Research Paper Award Session					
Panorama 2		Opening Session	ISC 2018 Conference Keynote	TOP500			Vendor Showdown 01		Future Accelerated Math Library Design	Visualization and HPC				
Panorama 3							Vendor Showdown 02		What's new with Cloud Computing for HPC	Panel: Will HPC Transform AI? Or Will AI Transform HPC?	Intel HPC Special			
Pikkolo							BoF: Preparing to Liquid Cool Your Next HPC Deployment	BoF: XALT and Related Technologies: Job-Level Usage ...	BoF: ARM for HPC Co-Design Opportunities	BoF: Physics and Engineering Sciences Workflow ...				
Booth N-210									Exhibitor Forum					
Booth N-230									Project Posters					
Exhibition Hall									Exhibition		ISC Welcome Party			
■ CONFERENCE PASS ■ EXHIBITION PASS ■ TUTORIAL PASS ■ WORKSHOP PASS														
Program may be subject to changes; the latest version of the program is available at <a href="https://2018.isc-program.com/">https://2018.isc-program.com/</a>														

Panorama 2 + 3

08:30 am - 09:00 am **■ Opening Session** Panorama 2 + 3

*Chairs:*

*Martin Meuer, ISC Group*

*Thomas Meuer, ISC Group*

08:30 am - 08:35 am Welcome & Introduction ISC 2018

*Martin Meuer, ISC Group*

*Thomas Meuer, ISC Group*

08:35 am - 08:45 am ISC 2018 Program

*Horst Simon, Lawrence Berkeley National Laboratory*

08:45 am - 08:55 am Announcement of the Two Finalists of the  
Hans Meuer Award & Announcement of the Gauss Award

*David Keyes, KAUST*

08:55 am - 09:00 am Towards providing Exascale Capacity to Scientists

*Serge Bogaerts, PRACE AISBL*

09:00 am - 10:00 am **■ ISC 2018 Conference Keynote** Panorama 2 + 3

*Chair:*

*Horst Simon, Lawrence Berkeley National Laboratory*

09:00 am - 10:00 am Tackling tomorrow's Computing Challenges  
today at CERN

*Maria Girone, CERN - European Organization  
for Nuclear Research*



10:45 am - 11:45 am **■ TOP500** Panorama 2 + 3

*Chair:*

*Horst Simon, Lawrence Berkeley National Laboratory*

10:45 am - 10:55 am TOP500 & Green500 Awards

*Horst Simon, Lawrence Berkeley National Laboratory*

*Jack Dongarra, Innovative Computing Laboratory; EECS Department;  
University of Tennessee*

*Wu Feng, Virginia Tech*

*Martin Meuer, ISC Group*

*Erich Strohmaier, Lawrence Berkeley National Laboratory*

General Information

Sunday, June 24

Monday, June 25

Tuesday, June 26

Wednesday, June 27

Thursday, June 28

10:55 am - 11:15 am	Highlights of the 51st TOP500 List <i>Erich Strohmeier, Lawrence Berkeley National Laboratory</i>
11:15 am - 11:45 am	Panel: TOP500's Relevance after 25 Years <i>Moderator:</i> <i>Horst Simon, Lawrence Berkeley National Laboratory</i> <i>Panelists:</i> <i>Steve Conway, Hyperion Research</i> <i>Yutong Lu, NUDT, National Supercomputer Center in Guangzhou</i> <i>Thomas Schulthess, Swiss National Supercomputing Center</i> <i>Steve Scott, Cray</i>

Panorama 1

04:00 pm - 06:00 pm	<div>■ Research Paper Award Session</div> <div>Panorama 1</div> <div><i>Chairs:</i> <i>David Keyes, KAUST</i> <i>Horst Simon, Lawrence Berkeley National Laboratory</i></div>
04:00 pm - 04:10 pm	Introduction <i>Horst Simon, Lawrence Berkeley National Laboratory</i> <i>David Keyes, KAUST</i>
04:10 pm - 04:50 pm	Hans Meuer Award Finalist 1: Chebyshev Filter Diagonalization on Modern Manycore Processors and GPGPUs <i>Gerhard Wellein, University of Erlangen-Nuremberg</i>
04:50 pm - 05:30 pm	Hans Meuer Award Finalist 2: Compiler-assisted Source-to-Source Skeletonization of Application Models for System Simulation <i>Jeremiah Wilke, Sandia National Labs</i>
05:30 pm - 06:00 pm	Gauss Award Winning Paper: On the Accuracy and Usefulness of Analytic Energy Models for Contemporary Multicore Processors <i>Johannes Hofmann, University of Erlangen-Nuremberg</i>

Panorama 2		
01:00 pm - 03:00 pm	<b>■ Vendor Showdown 01</b> <b>Moderators:</b> <i>Rupak Biswas, NASA Ames Research Center</i> <i>Addison Snell, Intersect360 Research</i>	Panorama 2
01:00 pm - 01:05 pm	<b>Introduction</b> <i>Addison Snell, Intersect360 Research</i> <i>Rupak Biswas, NASA Ames Research Center</i>	
01:05 pm - 01:15 pm	<b>Hewlett Packard Enterprise</b> <i>Jean-Luc Assor, Hewlett Packard Enterprise</i>	
01:15 pm - 01:25 pm	<b>Huawei</b> <i>Francis Lam, Huawei</i>	
01:25 pm - 01:35 pm	<b>IBM</b> <i>Sumit Gupta, IBM</i>	
01:35 pm - 01:45 pm	<b>Lenovo</b> <i>Scott Tease, Lenovo Data Center Group</i>	
01:45 pm - 01:55 pm	<b>NVIDIA GmbH</b> <i>Marc Hamilton, Nvidia</i>	
01:55 pm - 02:05 pm	<b>Supermicro</b> <i>Martin Galle, Supermicro</i>	
02:05 pm - 02:15 pm	<b>Iceland for HPC: Anything is Possible</b> <i>Anastasia Alexandersdottir, Opin Kerfi</i>	
02:15 pm - 02:25 pm	<b>Cavium</b> <i>Gopal Hegde, Cavium</i>	
02:25 pm - 02:35 pm	<b>Fujitsu</b> <i>Toshiyuki Shimizu, Fujitsu Limited</i>	
02:35 pm - 02:45 pm	<b>Quantum</b> <i>Molly Presley, Quantum Corporation</i>	
02:45 pm - 02:55 pm	<b>Inspur</b> <i>To be announced</i>	
02:55 pm - 03:00 pm	<b>Voting Results &amp; Awarding</b> <i>Addison Snell, Intersect360 Research</i> <i>Rupak Biswas, NASA Ames Research Center</i>	

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04:00 pm - 05:00 pm	<div><div>■ Future Accelerated Math Library Design</div><div>Chair: <i>Jack Dongarra, Innovative Computing Laboratory; EECS Department; University of Tennessee</i></div></div>	Panorama 2
04:00 pm - 04:20 pm	<div>Numerical Linear Algebra for Future Extreme-Scale Systems (NLAFET) <i>Jack Dongarra, Innovative Computing Laboratory; EECS Department; University of Tennessee</i></div>	
04:20 pm - 04:40 pm	<div>Accelerated Sparse Linear Algebra: Some Lessons, Challenges and Opportunities <i>Michael Heroux, Sandia National Laboratories, St. Johns University</i></div>	
04:40 pm - 05:00 pm	<div>Numerical Library with High-Performance/ Adaptive-Precision/High-Reliability: Extension of ppOpen-HPC towards the Post Moore Era <i>Kengo Nakajima, The University of Tokyo/RIKEN R-CCS</i></div>	
05:00 pm - 06:00 pm	<div><div>■ Visualization and HPC</div><div>Chair: <i>Jean M. Favre, Swiss National Supercomputing Center</i></div></div>	Panorama 2
05:00 pm - 05:20 pm	<div>Visualization in Earth System Research - Current Status and Challenges imposed by HPC Trends <i>Michael Böttinger, Deutsches Klimarechenzentrum (DKRZ)</i></div>	
05:20 pm - 05:40 pm	<div>Visualizing the Atomic Detail Dynamics of Biomolecular Complexes in Our Compute-Rich but I/O-Constrained Future <i>John E. Stone, University of Illinois at Urbana-Champaign</i></div>	
05:40 pm - 06:00 pm	<div>Preparations for Exascale Visualization at DOE <i>Kenneth Moreland, Sandia National Laboratories</i></div>	
06:00 pm - 06:45 pm	<div><div>■ Intel HPC Special</div><div>New Beowulf: The Bull or the Bear? <i>Rajeeb Hazra, Intel</i></div></div>	Panorama 2

Panorama 3		
01:00 pm - 03:00 pm	<b>■ Vendor Showdown 02</b> <b>Moderators:</b> <i>Dan Olds, Gabriel Consulting Group</i> <i>Mark Parsons, University of Edinburgh</i>	Panorama 3
01:00 pm - 01:05 pm	Introduction <i>Mark Parsons, EPCC, The University of Edinburgh</i> <i>Dan Olds, Gabriel Consulting Group</i>	
01:05 pm - 01:15 pm	Atos <i>Jean-Pierre Panziera, Atos</i>	
01:15 pm - 01:25 pm	Intel <i>Avinash Palaniswamy (Nash), Intel</i>	
01:25 pm - 01:35 pm	Samsung Semiconductor Europe <i>Thomas Arenz, Samsung Semiconductor Europe</i>	
01:35 pm - 01:45 pm	Cray <i>Steve Scott, Cray Inc.</i>	
01:45 pm - 01:55 pm	NEC Deutschland GmbH <i>Shintaro Momose, NEC Corporation</i>	
01:55 pm - 02:05 pm	Mellanox <i>Gilad Shainer, Mellanox Technologies</i>	
02:05 pm - 02:15 pm	DDN Storage <i>Jan Heichler, DDN Storage</i>	
02:15 pm - 02:25 pm	Dell EMC <i>Ed Turkel, Dell EMC</i>	
02:25 pm - 02:35 pm	Google <i>Kevin Kissell, Google Inc.</i>	
02:35 pm - 02:45 pm	Oracle <i>Taylor Newill, Oracle</i>	
02:45 pm - 02:55 pm	NetApp <i>Stan Skelton, NetApp</i>	
02:55 pm - 03:00 pm	Voting Results & Awarding <i>Mark Parsons, EPCC, The University of Edinburgh</i> <i>Dan Olds, Gabriel Consulting Group</i>	

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04:00 pm - 05:00 pm	<div><div>■ What's new with Cloud Computing for HPC</div><div><i>Chairs:</i> <i>Natalie Bates, Energy Efficient HPC Working Group,</i> <i>Lawrence Livermore National Laboratory</i> <i>John Shalf, Lawrence Berkeley National Laboratory</i></div></div>	Panorama 3
04:00 pm - 04:20 pm	Constrained Computing at the Edge <i>Daniel A. Reed, University of Iowa</i>	
04:20 pm - 04:40 pm	Science as a Service: Delivering Constraint-Based Computing Services to Industry <i>Alison Kennedy, STFC Hartree Centre</i>	
04:40 pm - 05:00 pm	The Value of Data: Using Constraints to Drive Efficiencies in HPC and Cloud Computing <i>Gregory Koenig, KPMG, Energy Efficient HPC Working Group</i>	
05:00 pm - 06:00 pm	<div><div>■ Panel: Will HPC Transform AI or Will AI Transform HPC?</div><div><i>Moderator:</i> <i>John Shalf, Lawrence Berkeley National Laboratory</i> <i>Panelists:</i> <i>Peter Messmer, NVIDIA</i> <i>Alessandro Curioni, IBM</i> <i>Satoshi Matsuoka, RIKEN, Tokyo Institute of Technology</i></div></div>	Panorama 3



Analog 1 + 2

01:00 pm - 03:00 pm	<b>■ PhD Forum</b> <p><i>Chairs:</i>  <b>Florina Ciorba, University of Basel</b>  <b>Gerhard Wellein, Erlangen Regional Computing Center,</b>  <b>University of Erlangen-Nuremberg</b></p>	Analog 1 + 2
01:00 pm - 01:05 pm	Welcome & Introduction <i>Gerhard Wellein, Erlangen Regional Computing Center,</i> <i>University of Erlangen-Nuremberg</i> <i>Florina Ciorba, University of Basel</i>	
01:05 pm - 01:09 pm	(PhD01) Modelling of Dynamic Network Objects: New Approaches and Adaptation Challenges for Future HPC Systems <i>Volodymyr Kushnarenko, Ulm University</i>	
01:09 pm - 01:13 pm	(PhD02) Mixed precision multi-level approach for FFTs <i>Malte Brunn, University of Stuttgart,</i> <i>Institute for Parallel and Distributed Systems</i>	
01:13 pm - 01:17 pm	(PhD03) Accelerators in a Hybrid HPC World: How Can Applications Benefit? <i>Antonio Maffia, University of Basel</i>	
01:17 pm - 01:21 pm	(PhD04) Global Tasking Data Dependencies for PGAS Applications <i>Joseph Schuchart, HLRS, University of Stuttgart</i>	
01:21 pm - 01:25 pm	(PhD05) Software-Based Fault Tolerance Techniques for Exascale HPC Systems <i>Grzegorz Pawelczak, University of Bristol</i>	
01:25 pm - 01:29 pm	(PhD06) A First Principles Approach to Performance and Power Models for Contemporary Multi- and Many-Core Processors <i>Johannes Hofmann, University of Erlangen-Nuremberg</i>	
01:29 pm - 01:33 pm	(PhD07) Pystencils - Automatic Generation, Optimization and Analysis of Stencil Codes <i>Jan Hönig, University of Erlangen-Nuremberg</i>	
01:33 pm - 01:37 pm	(PhD08) A Machine Learning Workflow for Hurricane Prediction <i>Albert N. Kahira, Barcelona Supercomputing Center,</i> <i>Universitat Politècnica de Catalunya</i>	
01:37 pm - 01:41 pm	(PhD09) Bringing Neuroscience to HPC <i>George Chatzikonstantis, National Technical University of Athens (NTUA); Institute of Communications and Computer Systems (ICCS)</i>	
01:41 pm - 01:45 pm	(PhD10) Quantification of Load Balancing related Performance Optimizations <i>Monika Harlacher, University of Siegen</i>	

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01:45 pm - 01:49 pm	(PhD11) Numerical Simulation of Flow around Complex Geometries using immersed Boundaries and DG <i>Neda Ebrahimi Pour, University of Siegen</i>
01:49 pm - 01:53 pm	(PhD12) From Molecular Dynamics towards a Node-Level Auto-Tuning Library for N-Body Simulations <i>Fabio Gratl, Technical University Munich</i>
01:53 pm - 01:57 pm	(PhD13) Parallel Low Memory Footprint Eikonal Solver in Cardiovascular Applications <i>Daniel Ganellari, Institute of Mathematics and Scientific Computing, University of Graz</i>
01:57 pm - 02:01 pm	(PhD14) Hybrid Hierarchical Data Management System: Accelerating Data Processing on HPC Systems <i>Peng Cheng, National University of Defense Technology</i>
02:01 pm - 02:05 pm	(PhD15) HasFS: A File System for NVM-based Hybrid Storage Architecture <i>Yubo Liu, Sun Yat-Sen University, National Supercomputer Center in Guangzhou</i>
02:05 pm - 02:50 pm	PhD Forum Poster Session
02:50 pm - 03:00 pm	ISC 2018 PhD Forum Award Ceremony <i>The ISC 2018 PhD Forum Award is sponsored by Springer, the international publisher specializing in science, technology and medicine.</i>

Konstant

01:00 pm - 01:30 pm	<div>ISC Orientation Session</div> <div>Speakers: Kimberly McMahon, McMahon Consulting Nages Sieslack, ISC Group</div>	Konstant
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Pikkolo

01:00 pm - 02:00 pm	<div>BoF: Preparing to Liquid Cool Your Next HPC Deployment</div> <div>Speaker: Patrick McGinn, CoolIT Systems</div>	Pikkolo
02:00 pm - 03:00 pm	<div>BoF: XALT and Related Technologies: Job-Level Usage Data on Today's Supercomputers</div> <div>Speaker: Robert McLay, TACC</div>	Pikkolo

04:00 pm - 05:00 pm	<b>■ BoF: ARM for HPC Co-Design Opportunities</b> <i>Speakers:</i> <i>Stephen Poole, Los Alamos National Laboratory</i> <i>Jeffrey Young, Georgia Institute of Technology</i> <i>Oscar Hernandez, Oak Ridge National Laboratory</i> <i>Filippo Mantovani, Barcelona Supercomputing Center</i> <i>Mitsuhisa Sato, RIKEN</i> <i>Simon Hammond, Sandia National Laboratories</i> <i>Dirk Pleiter, Jülich Supercomputing Centre (JSC)</i> <i>Jonathan Beard, Arm Research</i> <i>Guillaume Colin de Verdière, CEA</i>	Pikkolo
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05:00 pm - 06:00 pm	<b>■ BoF: Physics and Engineering Sciences Workflow at Scale - Practice and Challenges</b> <i>Speakers:</i> <i>Dan Laney, Lawrence Livermore National Laboratory</i> <i>Giovanni Pizzi, EPFL</i> <i>George Orient, Sandia National Laboratory</i>	Pikkolo
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#### Booth N-210

03:20 pm - 06:20 pm	<b>■ Exhibitor Forum</b> Altair: Access, Control, and Optimize HPC Clusters & Clouds with PBS Works 2018 <i>Bill Nitzberg, Altair</i> Boston Limited: HPC and The SuperCloud <i>David Power, Boston Limited</i> GiDEL: Different Methodologies for Optimizing Algorithms for FPGA Processing <i>Reuven Weintraub, GiDEL</i> National Supercomputer Center in Guangzhou (NSCCGZ) Tianhe-Starlight: Leading HPC into the Era of Cloud Computing <i>Yufei Du, NSCC-GZ</i> National University of Defense Technology (NUDT): TianHe-2A and the future TianHe-3 <i>Ruibo Wang, National University of Defense Technology (NUDT)</i>	Booth N-210
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05:40 pm - 06:00 pm	Thercon-LHP: To be announced <i>To be announced</i>
06:00 pm - 06:20 pm	Atempo: Protecting Massive File-Based Data Sets and Improving Data Availability with reduced TCO <i>Hervé Collard, Atempo</i> <i>Thomas Ahrens, Atempo</i>

Booth N-230

03:00 pm - 08:30 pm	<b>■ Project Posters</b> For a complete list of the Project Posters on display at ISC 2018, please refer to page 52	Booth N-230
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Exhibition Hall

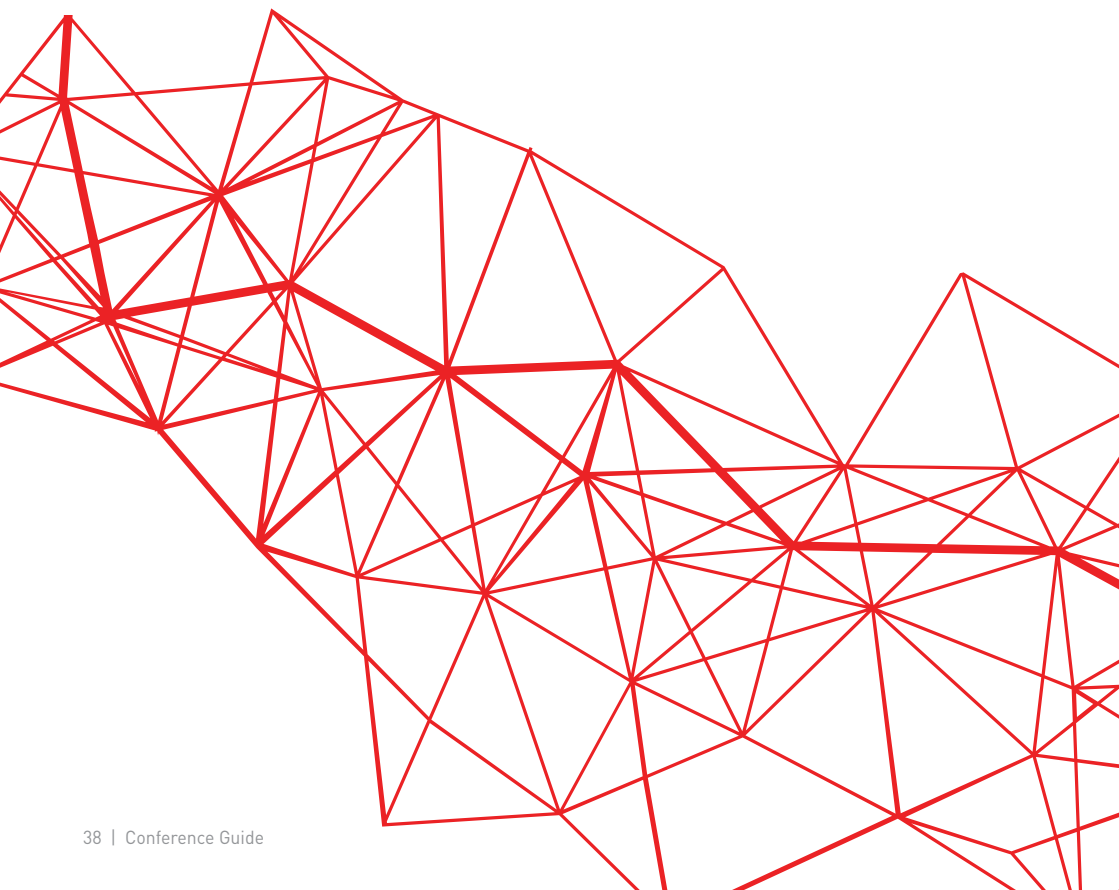
03:00 pm - 08:30 pm	<b>■ Exhibition</b>	Exhibition Hall
06:30 pm - 08:30 pm	<b>■ ISC Welcome Party</b> The ISC Welcome Party is famous for its food, drinks, entertainment and amiable atmosphere. It gives you the opportunity to visit vendors and service providers of your choice, and also socialize with other HPC enthusiasts. An entertaining party duo - consisting of a DJane and a saxophonist - will be on the show floor this year, to add swing and groove to the exhibition opening. We look forward to seeing you there!	Exhibition Hall

Coffee & Lunch Breaks

07:30 am - 08:30 am	Welcome Coffee	Panorama Foyer
10:00 am - 10:45 am	Coffee Break	Areal
11:45 am - 01:00 pm	Lunch Break	Areal
03:00 pm - 04:00 pm	Coffee Break	Exhibition Hall

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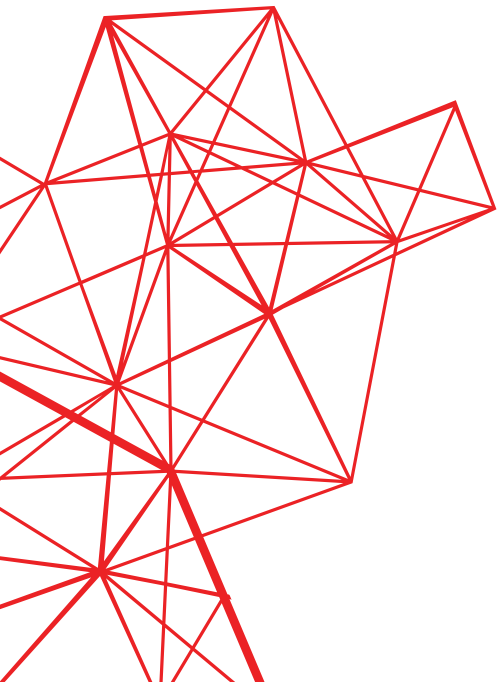


# PROGRAM

Conference

(in chronological order per room)

TUESDAY,  
JUNE 26



ROOM	8am	9am	10am	11am	12pm	1pm	2pm	3pm	4pm	5pm	6pm	7pm	8pm	9pm		
Analogue 1 + 2		Research Paper Session - HPC Applications / Architectures & Networks		Research Paper Session - AI & Machine Learning / HPC Algorithms			Research Paper Session - Programming Models and System Software		BoF: HPC Education: Widening Participation ...							
Areal			Reception & ISC Research Poster Awards	Research Posters + PhD Forum Posters												
Substanz 1 + 2		Research Posters Session	BoF: HPC, Containers and Big Data Analytics ...	BoF: The ID-500 and the Virtual Institute of IO			BoF: The Message Passing Interface ...	BoF: Artificial Intelligence and Performance Analysis ...	BoF: Multi-Level Memory and Storage for HPC and Data ...							
Panorama 1 INDUSTRIAL DAY		Cloud Services on Top of Classical Cloud Provider		Digital Twins			Industrial Usage of Public HPC Centres		Industrial HPC User Panel							
Panorama 2		Pushing Digital Computing to the Limits		Quantum Computing		German HPC in Context	Upcoming Exascale Systems		Distinguished Speakers	H. M. Award	Tuesday Keynote					
Panorama 3		HPC in Medicine Computational Approaches to Cancer Developments		What is Smart? HPC, IoT and AI for Smart Cities and Smart Mobility			Tornados, Disaster & Early Warnings		Cyber Security							
Pikkolo		BoF: ESWARE: HPC Computing for Weather and Climate? Trends in Energy Efficient and Resource ...	BoF: The GreenJobs: Trends in Energy Efficient and Resource ...	BoF: Energy and Power Aware: \$3.0 is Nearly Job Scheduling Here - Find out All About it!			BoF: Batched BLAS Standardization	BoF: LUSTRE in HPC and Big Data: Roadmap, Implementation Features ...	BoF: BeoGPS - Architecture, Implementation Examples ...							
Booth N-210							Exhibitor Forum									
Booth N-230							Project Posters	Project Posters Presentation	Project Posters							
Exhibition Hall							Exhibition									

CONFERENCE PASS

EXHIBITION PASS

TUTORIAL PASS

WORKSHOP PASS

Program may be subject to changes; the latest version of the program is available at <https://2018.isc-program.com/>



Panorama 1 - INDUSTRIAL DAY		
08:30 am - 10:00 am	<b>■ Cloud Services on Top of Classical Cloud Provider</b> <b>Chair:</b> <i>Gerd Buettner, Airbus Operation GmbH</i>	Panorama 1 INDUSTRIAL DAY
08:30 am - 09:00 am	Keynote: Using MantiumFlow as an Automation Tool for OpenFOAM to run external Car Aerodynamics CFD Simulations with Rotating Wheels in the Cloud <i>Andre Zimmer, MantiumCAE</i>	
09:00 am - 10:00 am	Cloud Services on Top of Classical Cloud Provider <i>Andre Stork, Fraunhofer</i> <i>David Ingersoll, Penguin</i> <i>Gabriel Broner, Rescale</i> <i>Karl-Heinz Hierholz, T-Systems</i> <i>Wolfgang Gentzsch, UberCloud</i>	
11:00 am - 12:30 pm	<b>■ Digital Twins</b> <b>Chair:</b> <i>Marie-Christine Sawley, Intel</i>	Panorama 1 INDUSTRIAL DAY
11:00 am - 11:22 am	Digital Twins and Artificial Intelligence <i>Englim Goh, HPE</i>	
11:22 am - 11:44 am	1.2 Million GE Digital Twins and Counting... Real Uses Cases of Productivity Boost for Industrial Companies <i>Andreas Neuhold, GE</i>	
11:44 am - 12:06 pm	Digital Twin and Smart Data – New Business Models in the Era of Digitalization <i>Thomas Hahn, Siemens</i>	
12:06 pm - 12:30 pm	Towards the 2nd Generation of Digital Twins mixing real-time Simulation in Operation and Artificial Intelligence <i>Jacques Duysens, Ansys</i>	
01:45 pm - 03:15 pm	<b>■ Industrial Usage of Public HPC Centres</b> <b>Chair:</b> <i>Alfred Geiger, T-Systems</i>	Panorama 1 INDUSTRIAL DAY
01:45 pm - 02:15 pm	Driving Adoption of HPC by SMEs in Europe <i>Mark Parsons, EPCC, The University of Edinburgh</i>	
02:15 pm - 02:45 pm	NCSA Industry-Overview and Computational Breakthroughs <i>Brendan McGinty, National Center for Supercomputing Applications (NCSA), University of Illinois</i> <i>Seid Koric, National Center for Supercomputing Applications (NCSA), University of Illinois</i>	

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02:45 pm - 03:15 pm	Simulation and Validation of Watermanagement in Vehicle Development using High Performance Computing <i>Steffen Hagmann, Porsche AG</i>	
03:45 pm - 04:45 pm	<div><div><div>■ Panel: Industrial HPC User Panel</div><div>Moderator: <i>Andreas Wierse, SICOS BW GmbH</i></div><div>Panelists: <i>Claudio Arlandini, CINECA</i> <i>Keith Foston, Siemens AG</i> <i>Stephan Schenk, BASF SE</i> <i>Steffen Poppitz, Robert Bosch GmbH</i> <i>Brendan McGinty, NCSA</i></div></div></div>	Panorama 1 INDUSTRIAL DAY
Panorama 2		
08:30 am - 10:00 am	<div><div><div>■ Pushing Digital Computing to the Limits</div><div>Chair: <i>Andreas Stiller, A20-Gate</i></div></div></div>	Panorama 2
08:30 am - 08:52 am	Technology Challenges and Trends over the next Decade (A Look through a 2030 Crystal Ball) <i>Al Gara, Intel</i>	
08:52 am - 09:14 am	Future-Proof: GPU-Accelerated Computing Beyond Moore's Law <i>Steve Oberlin, NVIDIA</i>	
09:14 am - 09:36 am	The Future of Vector Processing <i>Rudolf Fischer, NEC Deutschland GmbH</i>	
09:36 am - 10:00 am	Adaptable Compute Acceleration for Future Workloads <i>Ivo Bolsens, Xilinx</i>	
11:00 am - 12:30 pm	<div><div><div>■ Quantum Computing</div><div>Chair: <i>Matthias Troyer, Microsoft</i></div></div></div>	Panorama 2
11:00 am - 11:22 am	Q# Software Libraries and Applications for Scalable Quantum Computers <i>Martin Roetteler, Microsoft</i>	
11:22 am - 11:44 am	The ProjectQ Software Framework for Quantum Computing <i>Thomas Haener, ETH Zurich</i>	
11:44 am - 12:06 pm	Quantum Computing at Google <i>Kevin D. Kissell, Google</i>	
12:06 pm - 12:30 pm	Quantum Computing at IBM <i>Paul Nation, IBM - T. J. Watson</i>	

12:30 pm - 01:00 pm	<b>■ German HPC in Context</b> <i>Chair:</i> <b>Michael M. Resch, HLRS High Performance Computing Center Stuttgart</b>	Panorama 2
01:45 pm - 03:15 pm	<b>■ Upcoming Exascale Systems</b> <i>Chair:</i> <b>Yutong Lu, NUDT, National Supercomputer Center in Guangzhou</b> <b>The U.S. Department of Energy Exascale Computing Project</b> <i>Douglas Kothe, Oak Ridge National Laboratory</i> <b>Update on Post-K Development</b> <i>Yutaka Ishikawa, RIKEN AICS</i> <b>EuroHPC, Exascale for Europe</b> <i>Sergi Girona, BSC, Barcelona Supercomputing Center</i> <b>A Blueprint of Road to Exascale in China</b> <i>Kai Lu, NUDT</i>	Panorama 2
01:45 pm - 02:07 pm		
02:07 pm - 02:29 pm		
02:29 pm - 02:51 pm		
02:51 pm - 03:15 pm		
03:45 pm - 04:45 pm	<b>■ Distinguished Speakers</b> <b>Redesigning Climate Model on Sunway TaihuLight for Peta-Scale Performance and Ultra-High Resolution</b> <i>Lin Gan, Tsinghua University,</i> <i>National Supercomputing Center in Wuxi</i> <b>Incentives and Games in Large Scale Mobility</b> <i>Alexandre M. Bayen, University of California, Berkeley</i>	Panorama 2
03:45 pm - 04:15 pm		
04:15 pm - 04:45 pm		
05:00 pm - 05:15 pm	<b>■ Hans Meuer Award Ceremony</b> <i>Chairs:</i> <b>David Keyes, KAUST</b> <b>Horst Simon, Lawrence Berkeley National Laboratory</b>	Panorama 2
05:15 pm - 06:00 pm	<b>■ Tuesday Keynote</b> <i>Chair:</i> <b>Satoshi Matsuoka, RIKEN, Tokyo Institute of Technology</b>	Panorama 2
05:15 pm - 06:00 pm	<b>Empowering flexible and scalable High Performance Architectures with embedded Photonics</b> <i>Keren Bergman, Columbia University</i>	

Panorama 3		
08:30 am - 10:00 am	<b>HPC in Medicine Computational Approaches to Cancer Developments</b> <i>Chair:</i> <i>Tilo Wettig, University of Regensburg</i>	Panorama 3
08:30 am - 09:00 am	Precision Medicine and the AI/BD/HPC Nexus <i>Dimitri Kusnezov, US Department of Energy/NNSA</i>	
09:00 am - 09:30 am	Discovery of two States of Neuroblastoma Cells via the Analysis of Super-Enhancer Landscape <i>Valentina Boeva, Inserm U1016, Institut Cochin</i>	
09:30 am - 10:00 am	Advances in Understanding Tumour Evolution through HPC <i>Katharina Jahn, ETH Zurich</i>	
11:00 am - 12:30 pm	<b>What Is Smart? HPC, IoT and AI for Smart Cities and Smart Mobility</b> <i>Chair:</i> <i>Frank Baetke, EOFS</i>	Panorama 3
11:00 am - 11:25 am	Urban Science and High Performance Computing <i>Mary Ann Piette, Lawrence Berkeley National Laboratory</i>	
11:25 am - 11:50 am	How Car Manufacturers Face Challenges of Urban Mobility by Changing the Way They Think and Act <i>Martin Hauschild, BMW Group</i>	
11:50 am - 12:15 pm	Accelerating Singapore's AI Adoption <i>Laurence Liew, Singapore AI Industry Innovation</i>	
12:15 pm - 12:30 pm	Questions & Answers	
01:45 pm - 03:15 pm	<b>Tornados, Disaster &amp; Early Warnings</b> <i>Chair:</i> <i>Xavier Vigouroux, Atos / BULL SAS</i>	Panorama 3
01:50 pm - 02:12 pm	EC-Earth, a coupled Climate Model for extreme Event Prediction <i>Kim Serradell Maronda, Barcelona Supercomputing Center</i>	
02:12 pm - 02:34 pm	A Step Ahead: Novel HPC Approaches to provide Early Warnings for Severe Weather <i>Xavier Lapillonne, Meteoswiss</i>	
02:34 pm - 02:56 pm	Towards Global Cloud-Resolving Weather and Climate Prediction at Exascale <i>Philipp Neumann, German Climate Computing Center, University of Hamburg</i>	
02:56 pm - 03:15 pm	Wildfires Simulation, Front Evolution, Fire induced Weather and Pollution <i>Jean-Baptiste Filippi, CNRS, Università di Corsica</i>	

03:45 pm - 04:45 pm	<b>■ Cyber Security</b> <i>Chair:</i> <b><i>Dona Crawford, Lawrence Livermore National Laboratory (ret.)</i></b> Cyber Security Challenges and Opportunities in High-Performance Computing Environments <i>Sean Peisert, Lawrence Berkeley National Laboratory</i> To be announced <i>Arno Edelmann, Verizon</i> To be announced <i>To be announced</i>	Panorama 3
03:45 pm - 04:05 pm		
04:05 pm - 04:25 pm		
04:25 pm - 04:45 pm		

### Analog 1 + 2

08:30 am - 10:00 am	<b>■ Research Paper Session - HPC Applications / Architectures &amp; Networks</b> <i>Chairs:</i> <b><i>Martin Schulz, Technical University of Munich</i></b> <b><i>Antonino Tumeo, Pacific Northwest National Laboratory</i></b> Performance Optimization and Evaluation of Scalable Optoelectronics Application on Large Scale KNL Cluster <i>Yuta Hirokawa, University of Tsukuba</i> Megafly: A Topology For Exascale Systems <i>Mario Flajslik, Intel</i> Packetization of Shared-Memory Traces for Message Passing Oriented NoC Simulation <i>Davide Patti, University of Catania</i>	Analog 1 + 2
08:30 am - 09:00 am		
09:00 am - 09:30 am		
09:30 am - 10:00 am		
11:00 am - 12:30 pm	<b>■ Research Paper Session - AI &amp; Machine Learning / HPC Algorithms</b> <i>Chairs:</i> <b><i>Philipp Neumann, German Climate Computing Center, University of Hamburg</i></b> <b><i>Yu Wang, Leibniz Supercomputing Centre</i></b> Bayesian Optimization of HPC Systems for Energy Efficiency <i>Takashi Miyazaki, Yahoo Japan Corporation</i> Distributed Deep Reinforcement Learning: Learn how to Play Atari Games in 21 Minutes <i>Robert Adamski, Intel, Biz On Sp. z o.o.</i> <i>Tomasz Grel, deepsense.ai</i> Combining HTM with RCU to Speed Up Graph Coloring on Multicore Platforms <i>Christina Giannoula, National Technical University of Athens</i>	Analog 1 + 2
11:00 am - 11:30 am		
11:30 am - 12:00 pm		
12:00 pm - 12:30 pm		

01:45 pm - 03:15 pm	<p>■ <b>Research Paper Session - Programming Models and System Software</b></p> <p><i>Chair:</i>  <b>Sven-Bodo Scholz, Heriot-Watt University</b>  Heterogeneity-aware Resource Allocation in HPC Systems  <i>Alessio Netti, Department of Computer Science and Engineering, University of Bologna</i>  A Survey of Programming Tools for D-Wave Quantum-Annealing Processors  <i>Scott Pakin, Los Alamos National Laboratory</i>  DTF: An I/O Arbitration Framework for Multi-Component Data Processing Workflows  <i>Tatiana Martsinkevich, RIKEN AICS</i></p>	Analog 1 + 2
01:45 pm - 02:15 pm		
02:15 pm - 02:45 pm		
02:45 pm - 03:15 pm		
03:45 pm - 04:45 pm	<p>■ <b>BoF: HPC Education: Widening Participation and Increasing Skills through Contests, Challenges and Extra Curricular Learning</b></p> <p><i>Speakers:</i>  <b>Nitin Sukhija, Slippery Rock University of Pennsylvania</b>  <b>Maciej Cytowski, Pawsey Supercomputing Center</b>  <b>Scott Lathrop, University of Illinois</b>  <b>Victor Sande, Fundación Pública Galega Centro Tecnológico de Supercomputación de Galicia (CESGA)</b>  <b>Matthew L. Curry, Sandia National Laboratories</b>  <b>Benson Muite, University of Tartu</b>  <b>Maciej Szpindler, ICM, University of Warsaw</b>  <b>Bryan Johnston, Centre for high performance computing (CHPC)</b>  <b>Alexander Ditter, University of Erlangen-Nuremberg</b></p>	Analog 1 + 2
Areal		
10:00 am - 11:00 am	<p>■ <b>Reception &amp; ISC Research Poster Awards</b></p> <p>The seven ISC Research Poster Award Winners will be announced at the reception. The seven ISC Research Poster Awards are sponsored by <b>ISC Group</b>.</p>	Areal
11:00 am - 04:45 pm	<p>■ <b>Research Posters + PhD Forum Posters</b></p> <p>For a complete list of the Research Posters on display at ISC 2018, please refer to page 47.  For a complete list of the PhD Forum Posters on display at ISC 2018, please refer to page 33.</p>	Areal

Substanz 1 + 2		
08:30 am - 10:00 am	<p><b>■ Research Posters Session</b></p> <p><i>Chairs:</i>  <b>Sunita Chandrasekaran, University of Delaware</b>  <b>Matthias Müller, IT Center, RWTH Aachen University</b></p>	Substanz 1 + 2
08:30 am - 08:35 am	<p>Introduction  <i>Sunita Chandrasekaran, University of Delaware</i>  <i>Matthias Müller, IT Center, RWTH Aachen University</i></p>	
08:35 am - 09:55 am	<p><b>Architectures &amp; Networks</b></p> <p>(RP01) Can Unified-Memory support on Pascal and Volta GPUs enable Out-of-Core DNN Training?  <i>Hari Subramoni, The Ohio State University</i></p> <p>(RP02) Early Evaluation of a New Vector Processor SX-Aurora TSUBASA  <i>Kazuhiko Komatsu, Tohoku University</i></p> <p>(RP03) HPC with Unstructured Meshes on Novel Architectures  <i>Alban Lumi, University of Graz</i></p> <p>(RP04) GPU-based Parallel PO-SWE Algorithm for the Design of Large-sized Dual-Reflector Antennas  <i>Saki Matsuo, Mitsubishi Electric Corporation, Information Technology R&amp;D Center</i></p> <p>(RP05) Multicore Platform Efficiency Across Remote Sensing Applications  <i>Ekaterina Tyutlyaeva, RSC Technologies</i></p> <p><b>Artificial Intelligence and Machine Learning</b></p> <p>(RP06) A Deep Learning Tool for fast Simulation  <i>Sofia Vallecorsa, CERN - European Organization for Nuclear Research</i></p> <p><b>HPC Algorithms</b></p> <p>(RP07) GPU-accelerated Simulation of Elastic Wave Propagation  <i>Kristian Kadlubiak, Faculty of Information Technology, Brno University of Technology</i></p> <p>(RP08) Using GPU's FP16 Tensor Cores Arithmetic to Accelerate Mixed-Precision Iterative Refinement Solvers and Reduce Energy Consumption  <i>Azzam Haidar, University of Tennessee</i></p> <p>(RP09) iReceptor: A Platform for Exploring and Analyzing Antibody/B-cell and T-cell Receptor Repertoire Data across Federated Repositories  <i>Brian Corrie, Simon Fraser University</i></p> <p>(RP10) Deep Learning Hardware Accelerates Fused Discontinuous Galerkin Simulations  <i>Alexander Heinecke, Intel</i></p>	

- (RP11) Task-Parallel Domain-wise Multifrontal Solver  
for High-Performance Finite Element Analysis  
on Multi-core Systems

*Jeong Ho Kim, Inha University*

**Performance Modeling & Measurement**

- (RP12) A user-controlled GGDML Code Translation Technique  
for Performance Portability of Earth System Models

*Nabeeh Jum'ah, University of Hamburg*

- (RP13) Cross-architectural Modelling of Power Consumption  
Using Neural Networks

*Vadim Elisseev, IBM Research*

- (RP14) Automatic Generation of Full-Set Batched BLAS

*Yusuke Hirota, RIKEN Advanced Institute for  
Computational Science*

**Programming Models & Systems Software**

- (RP15) Automatic Classification of System Logs

*Siavash Ghiasvand, Technische Universität Dresden / ZIH*

- (RP16) HuronFS: Hierarchical, User-level and  
On-demand Burst Buffer File System

*Tianqi Xu, Tokyo Institute of Technology, National Institute  
of Advanced Industrial Science and Technology*

- (RP17) Event Driven Asynchronous Tasks (EDAT)

*Nick Brown, Edinburgh Parallel Computing Centre (EPCC)*

- (RP18) BONSAI (Benchtesting OpeN Software  
Autotuning Infrastructure)

*Piotr Luszczek, University of Tennessee,  
Innovative Computing Laboratory*

09:55 am – 10:00 am

Closing & Transition to the Poster Reception

*Matthias Müller, IT Center, RWTH Aachen University  
Sunita Chandrasekaran, University of Delaware*

10:00 am - 11:00 am

■ **BoF: HPC, Containers and Big Data Analytics –  
How can Cloud Computing contribute  
to the New Challenges**

Substanz 1 + 2

*Speakers:*

*Andrea Chierici, INFN-CNAF, Istituto Nazionale di Fisica Nucleare  
Jurry de la Mar, T-Systems International GmbH*

11:00 am - 12:00 pm

■ **BoF: The IO-500 and the Virtual Institute of I/O**

Substanz 1 + 2

*Speakers:*

*Julian Kunkel, University of Reading  
Jay Lofstead, Sandia National Laboratories  
John Bent, DDN*



01:45 pm - 02:45 pm	<b>■ BoF: The Message Passing Interface: Towards MPI 4.0 and Beyond</b> <i>Speaker:</i> <i>Martin Schulz, Technical University of Munich</i>	Substanz 1 + 2
02:45 pm - 03:45 pm	<b>■ BoF: Artificial Intelligence and Performance Analysis/Optimization</b> <i>Speakers:</i> <i>Hans-Christian Hoppe, Intel</i> <i>Felix Wolf, Technische Universität Darmstadt</i>	Substanz 1 + 2
03:45 pm - 04:45 pm	<b>■ BoF: Multi-Level Memory and Storage for HPC and Data Analytics</b> <i>Speakers:</i> <i>Hans-Christian Hoppe, Intel</i> <i>Michèle Weiland, Edinburgh Parallel Computing Centre</i> <i>Kathryn Mohror, Lawrence Livermore National Laboratory</i>	Substanz 1 + 2
<b>Pikkolo</b>		
08:30 am - 09:30 am	<b>■ BoF: ESIWACE: What is Special about HPC Computing for Weather and Climate?</b> <i>Speakers:</i> <i>Oliver Perks, Arm</i> <i>Reinhard Budich, Max-Planck-Institut für Meteorologie</i>	Pikkolo
09:30 am - 10:30 am	<b>■ BoF: The Green500: Trends in Energy Efficient Supercomputing</b> <i>Speakers:</i> <i>Wu Feng, Virginia Tech</i> <i>Erich Strohmaier, Lawrence Berkeley National Laboratory</i>	Pikkolo
10:30 am - 11:30 am	<b>■ BoF: Energy and Power Aware: Job Scheduling and Resource Management</b> <i>Speakers:</i> <i>Gregory Koenig, KPMG, Energy Efficient HPC Working Group</i> <i>Siddhartha Jana, Intel</i> <i>Milos Puzovic, The Hartree Centre</i> <i>Matthias Maiterth, Ludwig-Maximilians University of Munich, Intel</i>	Pikkolo
11:30 am - 12:30 pm	<b>■ BoF: OpenMP 5.0 is Nearly Here - Find out All About It!</b> <i>Speakers:</i> <i>James H. Cownie, Intel</i> <i>Michael Klemm, OpenMP ARB, Intel</i>	Pikkolo

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Thursday, June 28

01:45 pm - 02:45 pm	<p><b>■ BoF: Batched BLAS Standardization</b></p> <p><i>Speakers:</i>  <i>Jack Dongarra, Innovative Computing Laboratory; EECS Department; University of Tennessee</i>  <i>Mawussi Zounon, The University of Manchester</i>  <i>Sven Hammarling, The University of Manchester</i>  <i>Piotr Luszczek, Innovative Computing Laboratory</i>  <i>Pedro Valero-Lara, Barcelona Supercomputing Center</i></p>	Pikkolo
02:45 pm - 03:45 pm	<p><b>■ BoF: LUSTRE in HPC and Big Data: Roadmap, Features and Challenges</b></p> <p><i>Speakers:</i>  <i>Frank Baetke, EDFS</i>  <i>Sarp Oral, Oak Ridge National Laboratory</i></p>	Pikkolo
03:45 pm - 04:45 pm	<p><b>■ BoF: BeeGFS – Architecture, Implementation Examples, and Future Development</b></p> <p><i>Speakers:</i>  <i>Frank Herold, ThinkParQ GmbH</i>  <i>Frank Baetke, EDFS</i></p>	Pikkolo

#### Booth N-210

10:20 am - 05:40 pm	<b>■ Exhibitor Forum</b>	Booth N-210
10:20 am - 10:40 am	<p>UNIVA: Fair Weather, with an excellent Chance of Cloud Bursting - Migrating HPC Workloads to the Cloud</p> <p><i>Fritz Ferstl, UNIVA</i></p>	
10:40 am - 11:00 am	<p>Huawei: AI-Accelerated HPC Hardware Infrastructure</p> <p><i>Francis Lam, Huawei Enterprise USA</i></p>	
11:00 am - 11:20 am	<p>Cray: Artificial Intelligence – From Ideation to Impact</p> <p><i>Per Nyberg, Cray Inc.</i></p>	
11:20 am - 11:40 am	<p>NVIDIA GmbH: Deep Learning comes to HPC</p> <p><i>Axel Koehler, NVIDIA</i></p>	
11:40 am - 12:00 pm	<p>DDN Storage: Considerations in Architecting an AI Ready Data Platform</p> <p><i>James Coomer, DDN Storage</i></p>	
12:00 pm - 12:20 pm	<p>Dawning Information Industrie Co. Ltd (Sugon): Sugon's AI enabler: SothisAI and XSystem</p> <p><i>Qing Ji, SUGON</i></p>	
12:20 pm - 12:40 pm	<p>Google: TensorFlow Performance Optimizations</p> <p><i>Christian Sigg, Google Inc.</i></p>	
12:40 pm - 01:00 pm	<p>IBM: The World's Fastest Supercomputer</p> <p><i>James Sexton, IBM</i></p>	

01:00 pm - 01:20 pm	Dell EMC: To be announced <i>Ed Turkel, Dell EMC</i>
01:20 pm - 01:40 pm	Cavium: High-Performance Arm Processors for Exascale Computing <i>Surya Hotha, Cavium</i>
01:40 pm - 02:00 pm	Atos: Atos QLM, a future-proof Approach to Quantum Computing <i>Christelle Piechurski, Atos</i>
02:00 pm - 02:20 pm	Intel: Unleash the Future with HPC, HPDA & AI combined <i>Stephan Gillich, Intel</i>
02:20 pm - 02:40 pm	Hewlett Packard Enterprise: Unleash faster Insights using HPE's extreme Performance Solutions <i>Bill Mannel, Hewlett Packard Enterprise</i>
02:40 pm - 03:00 pm	Lenovo: AI for Enterprise: How to get started from Concept to Deployment <i>Boris Tvaroska, Lenovo Data Center Group</i>
03:00 pm - 03:20 pm	CoolIT Systems: Direct Liquid Cooling – Considerations for Data Center Operators <i>Patrick McGinn, CoolIT Systems</i>
03:20 pm - 03:40 pm	Motivair Corporation: Cooling Considerations for Liquid Cooling in HPC Applications <i>Rich Whitmore, Motivair Corporation</i>
03:40 pm - 04:00 pm	Iceland for HPC: Anything is Possible: Thinking ahead: Why Proactive Clean Energy Strategy Pays Off for Data Centers <i>Vala Valthorsdottir, Iceland for HPC: Anything is Possible</i>
04:00 pm - 04:20 pm	Asetek: Flexibility in Cooling Options: Brownfields & Greenfields <i>Larry Vertal, Asetek</i>
04:20 pm - 04:40 pm	Verne Global: TrueHPC delivered at industrial Scale <i>Tate Cantrell, Verne Global</i>
04:40 pm - 05:00 pm	Stäubli Tec-Systems GmbH Connectors: Connection Solutions for Liquid Cooling <i>Jean-Christophe Duisit, Stäubli Faverges</i>
05:00 pm - 05:20 pm	AMD: AMD's Radeon Instinct™ Accelerators for HPC & Machine Intelligence <i>Derek Bouius, AMD</i>
05:20 pm - 05:40 pm	PNY Technologies Europe: Meet AIRI Your first AI-Ready Infrastructure at Scale <i>Gautier Soubrane, PNY Technologies</i>

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Booth N-230

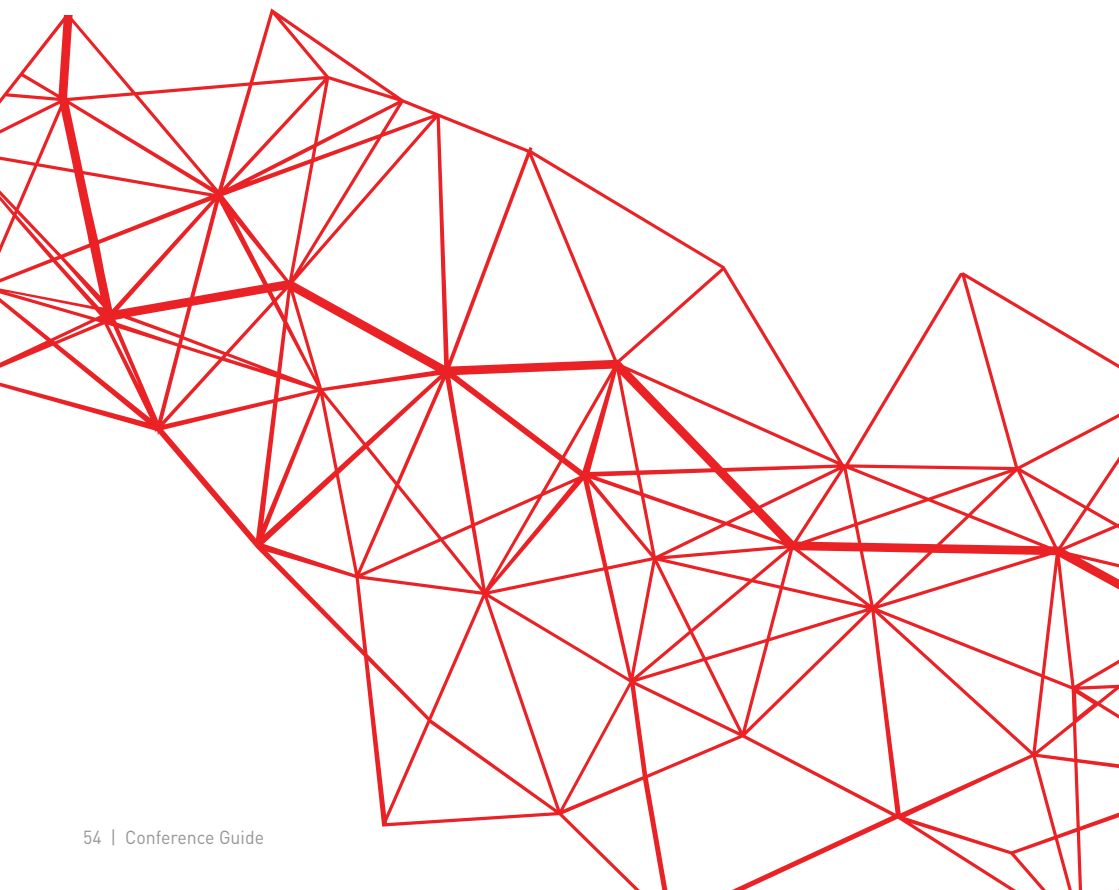
10:00 am - 06:00 pm	<p><b>■ Project Posters</b></p> <p>For a complete list of the Project Posters on display at ISC 2018, please see below.</p>	Booth N-230
03:15 pm - 03:45 pm	<p><b>■ Project Posters Presentation</b></p> <p><i>Chairs:</i>  <i>Julian Kunkel, University of Reading</i>  <i>Ekaterina Tyutlyayeva, RSC Tech</i></p> <p>(PP01) TaLPas: Task-Based Load Balancing and Auto-Tuning in Particle Simulations  (PP02) Inverse Coil Design by Machine Learning-based Optimization  (PP03) The Virtual Institute for I/O and the IO-500  (PP04) The High-Q Club  (PP05) The Fast Fourier Transform in the Exascale Era  (PP06) International HPC Certification Program  (PP07) ProPE – A joint Effort to Establish a unified Service Infrastructure for Performance Engineering in German HPC-Center  (PP08) Performance Conscious HPC (PeCoH) - 2018  (PP09) ProfiT-HPC: A Profiling Toolkit for HPC in Tiers 2 and 3  (PP10) ESIWACE: Centre of Excellence in Simulation of Weather and Climate in Europe  (PP11) EuroEXA: European Co-Design for Exascale Applications  (PP12) DEEP-HybridDataCloud  (PP13) An Auto Climate Model continuous Integration Strategy  (PP14) Mont-Blanc 2020, European Scalable, Modular and Power Efficient HPC Processor  (PP15) Energy Efficient Computing Research at UKRI Hartree  (PP16) Advanced Computation and I/O Methods for Earth-System Simulations (AIMES)  (PP17) Efficient Time Stepping in Partitioned Multi-Physics  (PP18) MYX - MUST Correctness Checking for YAML and XMP Programs  (PP19) Towards Supporting Heterogeneous Hardware in GROMACS  (PP20) ESSEX-II: Equipping Sparse Solvers for Exascale  (PP21) EXASTEEL: Multiscale Simulation of Steel using FE2TI  (PP22) A Deep Learning Tool for fast Simulation  (PP23) Decimate: A Portable and Fault-Tolerant Scheduler Extension Efficiently Handling a Large Number of Dependent Jobs  (PP24) Intelligent Digital Rock Modeling  (PP25) In Situ Visualization of Laser-Plasma Interaction</p>	Booth N-230

Exhibition Hall

10:00 am - 06:00 pm      ■ Exhibition      Exhibition Hall

Coffee & Lunch Breaks

07:30 am - 10:00 am	Welcome Coffee	Foyer Hall 3
10:00 am - 11:00 am	Coffee Break	Exhibition Hall
12:30 pm - 01:45 pm	Lunch Break	Exhibition Hall
03:15 pm - 03:45 pm	Coffee Break	Panorama Foyer, Exhibition Hall
04:45 pm - 05:15 pm	Coffee Break	Panorama Foyer, Exhibition Hall

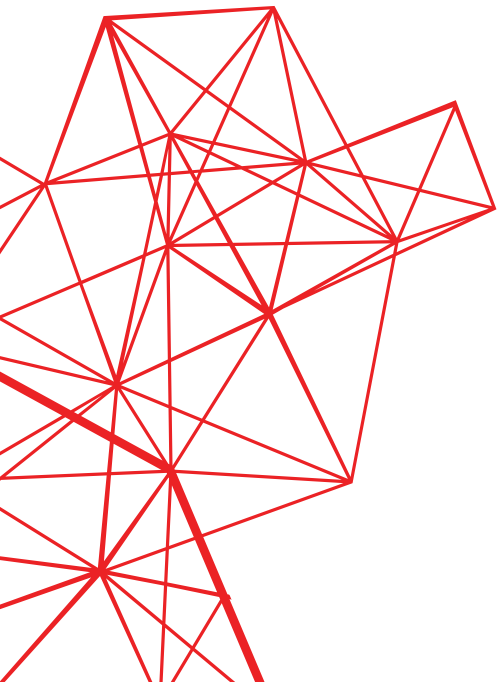


# PROGRAM

Conference

(in chronological order per room)

WEDNESDAY,  
JUNE 27



ROOM	8am	9am	10am	11am	12pm	1pm	2pm	3pm	4pm	5pm	6pm	7pm	8pm	9pm		
Analog 1 + 2		HPC in Asia Session 01	HPC in Asia Poster Session	HPC in Asia Session 02			Research Paper Session - Programming Models and ...	An Analysis of Contracts and...	Panel: Analyst Crossfire							
Areal		Research Posters + PhD Forum Posters + HPC in Asia Posters + Women in HPC Posters														
Konstant							BoF: Omni-Path User Group (OPUG) Meeting	BoF: SV-Aurora Large Bandwidth Vector Computing BoF								
Substanz 1 + 2		Research Paper Session - Performance Modelling and Measurement I		Research Paper Session - Performance Modelling and Measurement II			BoF: 16th Graph500 List	BoF: The Computational Hybrid Cloud Biomedicine Community ...	BoF: SSHaring Hybrid Cloud Computing Strategies ...							
Panorama 1		HPC in Medicine The Living Heart Project		Quantum Computing Applications			Material Sciences and HPC		Panel: Convergence of Extreme-Scale Computing ...							
Panorama 2 MACHINE LEARNING DAY		Keynotes Machine Learning Day		IO and Storage for Large Scale Machine Learning			Scalable Machine Learning Systems		HPC Software Stacks ...	SCC Awards	Wednesday Keynote	Closing				
Panorama 3		Astrophysics & HPC		Challenges for Developing & Supporting HPC Applications			Data Centric Computing		Distinguished Speakers							
Pikkolo		BoF: Reconfigurable Computing in HPC ...	BoF: Unified Communication X (UCX)	BoF: HPC Outreach - Growing the Eascale ...	BoF: Reinventing the Advantage of a diverse HPC Workforce		BoF: Building Efficient Clouds for HPC, Big Data ...	BoF: FFT in the Exascale: Opportunities and Challenges	BoF: PBS Pro Open Source Project Community BoF							
Booth N-210							Exhibitor Forum									
Booth N-230							Project Posters									
Exhibition Hall							Exhibition									
■ CONFERENCE PASS ■ EXHIBITION PASS ■ TUTORIAL PASS ■ WORKSHOP PASS																
Program may be subject to changes; the latest version of the program is available at <a href="https://2018.isc-program.com/">https://2018.isc-program.com/</a>																



Panorama 1		
08:30 am - 10:00 am	<b>■ HPC in Medicine The Living Heart Project</b> <i>Chair:</i> <b>Philipp Hempel, Admedes</b>	Panorama 1
08:30 am - 09:00 am	Fluid Structure Interaction Study for Hemodynamic Evaluation of Transcatheter Aortic Valve Replacement Device using HPC <i>Deepanshu Sodhani, Enmodes</i>	
09:00 am - 09:30 am	Investigation of Different Deployment Positions of a Generic Transcatheter Self-expandable Heart Valve Frame Using the Living Heart Model <i>Philipp Hempel, Admedes GmbH</i>	
09:30 am - 10:00 am	The Living Heart Project - Breaking the Barriers to Realistic Simulation of the Human Body <i>Björn Butz, Dassault Systèmes, SIMULIA</i>	
11:00 am - 12:30 pm	<b>■ Quantum Computing Applications</b> <i>Chair:</i> <b>Kristel Michielsen, Jülich Supercomputing Centre (JSC), RWTH Aachen University</b>	Panorama 1
11:00 am - 11:15 am	The Difference between Programming a Gate-Based Quantum Computer and a Quantum Annealer <i>Presenter: Kristel Michielsen, Institute for Advanced Simulation / Jülich Supercomputing Centre (JSC), RWTH Aachen University</i>	
11:15 am - 11:40 am	Material Simulation on current Quantum Computers <i>Michael Streif, Volkswagen Data:Lab</i>	
11:40 am - 12:05 pm	Flight Gate Assignment with a Quantum Annealer <i>Elisabeth Lobe, German Aerospace Center (DLR)</i>	
12:05 pm - 12:30 pm	Leveraging Adiabatic Quantum Computation for Election Forecasting <i>Maxwell Henderson, QxBranch LLC</i>	
01:45 pm - 03:15 pm	<b>■ Material Sciences and HPC</b> <i>Chair:</i> <b>Satoshi Matsuoka, RIKEN, Tokyo Institute of Technology</b>	Panorama 1
01:45 pm - 02:15 pm	Quantum Chemistry on the K Computer <i>Takahito Nakajima, RIKEN</i>	
02:15 pm - 02:45 pm	Discovering Novel Materials: The Convergence of HPC, HTC and Data Analytics <i>Nicola Marzari, EPFL</i>	
02:45 pm - 03:15 pm	Atomistic Simulations to understand and Mimic Biomaterials: Where the Nanoscale Matters <i>Frauke Gräter, Heidelberg University, Interdisciplinary Center for Scientific Computing (IWR); Heidelberg Institute for Theoretical Studies</i>	

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04:00 pm - 05:00 pm	<div><div>■ Panel: Convergence of Extreme-Scale Computing and Big Data – Looking Back and Looking Ahead</div><div>Moderator: Sadaf Alam, Swiss National Supercomputing Centre</div><div>Panelists: Jeff Nichols, Oak Ridge National Laboratory Haohuan Fu, National Supercomputing Center in Wuxi John Shalf, Lawrence Berkeley National Laboratory Simon McIntosh-Smith, University of Bristol</div></div>	Panorama 1
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Panorama 2 - MACHINE LEARNING DAY

08:30 am - 10:00 am	<div><div>■ Keynotes Machine Learning Day</div><div>Chairs: Janis Keuper, Fraunhofer ITWM Brian C. Van Essen, Lawrence Livermore National Laboratory</div></div>	Panorama 2 MACHINE LEARNING DAY
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08:30 am - 09:15 am	Summit Architecture for ML Jack Wells, Oak Ridge National Laboratory	
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09:15 am - 10:00 am	Towards Automated Deep Learning Frank Hutter, University of Freiburg	
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11:00 am - 12:30 pm	<div><div>■ IO and Storage for Large Scale Machine Learning</div><div>Chair: Janis Keuper, Fraunhofer ITWM</div></div>	Panorama 2 MACHINE LEARNING DAY
11:00 am - 11:30 am	Device Placement Optimization with Reinforcement Learning Azalia Mirhoseini, Google Brain	
11:30 am - 12:00 pm	Exploiting NVM for High-Performance I/O on POWER Architectures Dirk Pleiter, Jülich Supercomputing Centre (JSC)	
12:00 pm - 12:30 pm	HPC-DA: An Architecture on the Intersection between Flexibility and Maximum I/O Performance Michael Kluge, ZIH, Technische Universität Dresden	

01:45 pm - 03:15 pm	<b>■ Scalable Machine Learning Systems</b> <i>Chair:</i> <b>Brian C. Van Essen,</b> <i>Lawrence Livermore National Laboratory</i>	Panorama 2 MACHINE LEARNING DAY
01:45 pm - 02:15 pm	On Scale-out Deep Learning Training for Cloud and HPC <i>Pradeep Dubey, Intel</i>	
02:15 pm - 02:45 pm	Forging Large Scale AI Supercomputing - Inspiration from NVIDIA's SATURNV Supercomputer <i>Louis Capps, NVIDIA</i>	
02:45 pm - 03:15 pm	Machine Learning Hardware Targeted for Automotive Application: Requirements and Constraints <i>Bernhard Vogginger, Technische Universität Dresden</i>	
04:00 pm - 05:00 pm	<b>■ HPC Software Stacks for Machine Learning</b> <i>Chair:</i> <b>Janis Keuper, Fraunhofer ITWM</b>	Panorama 2 MACHINE LEARNING DAY
04:00 pm - 04:30 pm	ONNX, an open DNN Standard Format <i>Sarah Bird, Facebook</i>	
04:30 pm - 05:00 pm	Carme - An Open Source Framework for Multi-User, Interactive Machine Learning on Distributed GPU-Systems <i>Dominik Straßel, Fraunhofer Center Machine Learning, Fraunhofer ITWM</i>	
<b>Panorama 2</b>		
05:15 pm - 05:30 pm	<b>■ HPCAC-ISC Student Cluster Competition 2018 Award Ceremony</b> <i>Dan Olds, Gabriel Consulting Group</i> <i>Gilad Shainer, HPC Advisory Council</i>	Panorama 2
05:30 pm - 06:15 pm	<b>■ Wednesday Keynote</b> <i>Chair:</i> <b>Frank Baetke, EOFS</b>	Panorama 2
05:30 pm - 06:15 pm	HPC Achievement and Impact – 2018 <i>Thomas Sterling, School of Informatics, Computing, and Engineering Indiana University</i>	
06:15 pm - 06:30 pm	<b>■ Closing Session</b> <i>Chairs:</i> <b>Martin Meuer, ISC Group</b> <b>Thomas Meuer, ISC Group</b>	Panorama 2

Panorama 3		
08:30 am - 10:00 am	<div><div></div><div><b>Astrophysics &amp; HPC</b></div><div>Chair: <i>Edward Seidel, University of Illinois</i></div></div>	Panorama 3
08:30 am - 09:00 am	Binary Neutron Stars: Einstein's Richest Laboratory <i>Lurciano Rezzolla, Institute for Theoretical Physics</i>	
09:00 am - 09:30 am	Frontiers at the Interface of HPC, Artificial Intelligence and Multimessenger Astronomy <i>Eliu Huerta, NCSA, University of Illinois at Urbana-Champaign; Department of Astronomy, University of Illinois at Urbana-Champaign</i>	
09:30 am - 10:00 am	Software between Theory and Observation: Challenges and Strategies in Scientific Computing for relativistic Astrophysics <i>Eloisa Bentivegna, IBM Research UK</i>	
11:00 am - 12:30 pm	<div><div></div><div><b>Challenges for Developing &amp; Supporting HPC Applications</b></div><div>Chair: <i>William Gropp, University of Illinois at Urbana-Champaign</i></div></div>	Panorama 3
11:00 am - 11:15 am	Parallel Programming is the Easy Part: The Real Challenges in Developing HPC Applications <i>William Gropp, National Center for Supercomputing Applications and Department of Computer Science, University of Illinois at Urbana-Champaign</i>	
11:15 am - 11:40 am	To be announced <i>Torsten Hoefler, ETH Zurich</i>	
11:40 am - 12:05 pm	Bugs and Speed in HPC Applications: Past, Present, and Future <i>Jeffrey Hollingsworth, University of Maryland</i>	
12:05 pm - 12:30 pm	Modeling the Next-Generation High Performance Schedulers <i>Michela Taufer, University of Tennessee Knoxville, Department of Electrical Engineering and Computer Science</i>	
01:45 pm - 03:15 pm	<div><div></div><div><b>Data Centric Computing</b></div><div>Chair: <i>Torsten Hoefler, ETH Zurich</i></div></div>	Panorama 3
01:45 pm - 02:07 pm	Data Centric Programming Abstractions Suitable from a Multiphysics Application Perspective <i>Anshu Dubey, Argonne National Laboratory, University of Chicago</i>	
02:07 pm - 02:29 pm	Empirical Performance Modeling of HPC Workloads <i>Felix Wolf, Technische Universitaet Darmstadt</i>	

02:29 pm - 02:51 pm	Spatial Computing: A Synthesis of Data-Centric Computing and Extreme Hardware Specialization <i>John Shalf, Lawrence Berkeley National Laboratory</i>	
02:51 pm - 03:15 pm	Parallel Program = Operator + Schedule + Parallel Data Structures <i>Keshav Pingali, The University of Texas at Austin</i>	
<b>04:00 pm - 05:00 pm</b>	<b>■ Distinguished Speakers</b>	Panorama 3
04:00 pm - 04:30 pm	Simulation of Large-Scale Coupled Systems <i>Miriam Mehl, University of Stuttgart</i>	
04:30 pm - 05:00 pm	Blockchain: Technology Issues and Applications <i>Wolfgang Prinz, Fraunhofer FIT, RWTH Aachen University</i>	
Analog 1 + 2		
<b>08:30 am - 10:00 am</b>	<b>■ HPC in Asia Session 01</b>	Analog 1 + 2
	<i>Chair:</i> <b>Yutong Lu, NUDT, National Supercomputer Center in Guangzhou</b>	
08:30 am - 08:35 am	Welcome Address <i>Yutong Lu, NUDT, National Supercomputer Center in Guangzhou</i>	
08:35 am - 08:45 am	Status Report from China <i>Depei Qian, SYSU</i>	
08:45 am - 08:55 am	Status Report from Japan <i>Mitsuhisa Sato, RIKEN</i>	
08:55 am - 09:05 am	Status Report from Korea <i>Kwang-Jin Oh, KISTI</i>	
09:05 am - 09:15 am	Status Report from Qatar <i>Othmane Bouhali, Texas A&amp;M University at Qatar; Qatar Computing Research Institute, HBKU</i>	
09:15 am - 09:25 am	Status Report from Singapore <i>Stephen Wong, NSCC-SG</i>	
09:25 am - 09:35 am	Status Report from Taiwan <i>Weicheng Huang, NCHC</i>	
09:35 am - 09:45 am	Status Report from Thailand <i>Putchong Uthayopas, Kasetsart University</i>	
09:45 am - 09:55 am	Status Report from Saudi Arabia <i>David Keyes, KAUST</i>	
09:55 am - 10:00 am	Questions & Answers	

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10:00 am - 11:00 am

■ HPC in Asia Poster Session

Analog 1 + 2

*Chair:*

*Yutong Lu, NUDT, National Supercomputer Center in Guangzhou*

(A01) Poster from China: A Low Communication Overhead

Breadth-First Search based on Global Bitmap .pdf

*Yunfei Du, National Supercomputer Center in Guangzhou;*

*School of Data and Computer Science, Sun Yat-sen University*

(A02) Poster from China: SW-eMD: Molecular Dynamics

Simulation Framework on Sunway TaihuLight Supercomputer

*Shun Xu, Supercomputing Center, Chinese Academy of Sciences*

(A03) Poster from Egypt: Accelerated Reverse Time Migration  
with optimized IO

*Kareem Metwaly, Brightskies Inc.*

(A04) Poster from Japan: ppOpen-SOL: Robust ILU Preconditioner  
for Exascale

*Masatoshi Kawai, RIKEN R-CCS*

(A05) Poster from Japan: Accelerating HPC Applications  
on FPGAs using OpenCL and FPGA Network

*Norihisa Fujita, Center for Computational Sciences (CCS),  
University of Tsukuba*

(A06) Poster from Korea: Application Characterization by using  
Hardware Performance Counters with Data Mining

*Jieun Choi, Korea Institute of Science and Technology  
Information (KISTI)*

(A07) Poster from Qatar: Optimization and Parallelization  
of charged Particle Detector Simulation

*Faisal Chaudhry, Texas A&M University at Qatar*

(A08) Poster from Saudi Arabia: Hierarchical Computations  
on Manycore Architectures

*Hatem Ltaief, KAUST*

(A09) Poster from Singapore: Picking the Right One -  
Biosimulations on Various Architectures

*Jernej Zidar, Institute of High Performance Computing*

(A10) Poster from Taiwan: Simulation Studies of Sub-Terawatt  
Laser Wakefield Acceleration

*Chia-Ying Hsieh, National Central University (NCU)*

(A11) Poster from Taiwan: The Design and Implementation  
of Bare Metal Cluster Deployment Using BitTorrent

*Steven Shiau, National Center for High-Performance Computing*

(A12) Poster from Taiwan: The Development and  
Implementation of National Data Service Platform

*Weicheng Huang, National Center for High-Performance  
Computing, Taiwan*

11:00 am - 12:30 pm	<b>■ HPC in Asia Session 02</b> <i>Chair:</i> <b>Yutong Lu, NUDT, National Supercomputer Center in Guangzhou</b> Sophisticated urban Earthquake Simulation for enhancing Smart Cities <i>Tsuyoshi Ichimura, University of Tokyo, Earthquake Research Institute</i> Enabling Industrial CFD Simulations on Sunway TaihuLight <i>Haohuan Fu, Tsinghua University</i> Practices on HPC + AI from Huawei <i>Francis Lam, HUAWEI</i> Convergence and Integration of Data Science and HPC in Asia <i>Kengo Nakajima, The University of Tokyo/RIKEN R-CCS</i> HPC in Asia Poster Awarding & Closing	Analog 1 + 2
11:00 am - 11:20 am		
11:20 am - 11:40 am		
11:40 am - 12:00 pm		
12:00 pm - 12:20 pm		
12:20 pm - 12:30 pm		
01:45 pm - 02:45 pm	<b>■ Research Paper Session - Programming Models and System Software</b> <i>Chair:</i> <b>Sven-Bodo Scholz, Heriot-Watt University</b> TaskGenX: A Hardware-Software Proposal for Accelerating Task Parallelism <i>Kallia Chronaki, Barcelona Supercomputing Center</i> Classifying Jobs and Predicting Applications in HPC Systems <i>Keiji Yamamoto, RIKEN</i>	Analog 1 + 2
01:45 pm - 02:15 pm		
02:15 pm - 02:45 pm		
02:45 pm - 03:15 pm	<b>■ An Analysis of Contracts and Relationships between Supercomputing Centers and Electricity Service Providers</b> <i>Gregory Koenig, Energy Efficient HPC Working Group</i>	Analog 1 + 2
04:00 pm - 05:00 pm	<b>■ Panel: Analyst Crossfire</b> <i>Moderator:</i> <b>Addison Snell, Intersect360 Research</b> <i>Panelists:</i> <b>Depei Qian, Sun Yat-Sen University &amp; Beihang University</b> <b>Stephan Schenk, BASF SE</b> <b>Alex Bouzari, DDN</b> <b>Ian Colle, HPC at Amazon Web Services</b>	Analog 1 + 2

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## Areal

08:30 am - 05:00 pm	<p><b>■ Research Posters + PhD Forum Posters + HPC in Asia Posters + Women in HPC Posters</b></p> <p>For a complete list of the Research Posters on display at ISC 2018, please refer to page 47.</p> <p>For a complete list of the PhD Forum Posters on display at ISC 2018, please refer to page 33.</p> <p>For a complete list of the Women in HPC Posters on display at ISC 2018, please see below.</p>	Areal
08:30 am - 05:00 pm	<p><b>■ Women in HPC Posters</b></p> <p>(WHPC01) Performance Analysis of GS2 Plasma Turbulence Code <i>Sally Bridgwater, Numerical Algorithms Group</i> <i>Nick Dingle, Numerical Algorithms Group</i></p> <p>(WHPC02) High-volume Data Processing for Ambient Healthcare Research Prototyping <i>Emma Tonkin, University of Bristol</i></p> <p>(WHPC03) The Movement towards HPC inclusivity: Achieving on-demand Accessibility of High Performance Computing (HPC) in single-user, ephemeral Projects through the Alces Gridware Project <i>Cristin Merritt, Alces Flight Limited</i> <i>Wil Mayers, Alces Flight Limited</i> <i>Mark Titorenko, Alces Software Limited</i> <i>Steve Norledge, Alces Software Limited</i></p> <p>(WHPC04) DYNAMO - Dynamic Analysis Modelling and Optimisation of GDI engines <i>Aiman Batul Shaikh, STFC Hartree Centre</i></p>	Areal

## Konstant

01:45 pm - 02:45 pm	<p><b>■ BoF: Omni-Path User Group (OPUG) Meeting</b></p> <p><i>Speakers:</i> <i>Sergi Girona, BSC, Barcelona Supercomputing Center</i> <i>David Vicente, BSC, Barcelona Supercomputing Center</i> <i>Philip Murphy, Intel</i></p>	Konstant
02:45 pm - 03:45 pm	<p><b>■ BoF: SX-Aurora Large Bandwidth Vector Computing BoF</b></p> <p><i>Speakers:</i> <i>Erich Focht, NEC Deutschland GmbH</i> <i>Rudolf Fischer, NEC Deutschland GmbH</i> <i>Uwe Küster, HLRS High Performance Computing Center Stuttgart</i> <i>Tim Cramer, RWTH Aachen University</i></p>	Konstant



Substanz 1 + 2		
08:30 am - 10:00 am	<b>■ Research Paper Session - Performance Modelling and Measurement I</b> <b>Chair:</b> <i>Saday Sadayappan, The Ohio State University</i> The Pitfalls of Provisioning Exascale Networks: A Trace Replay Analysis for Understanding Communication Performance <i>Joseph Kenny, Sandia National Laboratories</i> Zeno: A Straggler Diagnosis System for Distributed Computing Using Machine Learning <i>Huanxing Shen, Intel</i> Hardware Performance Variation: A Comparative Study using Lightweight Kernels <i>Hannes Weisbach, Technische Universität Dresden</i>	Substanz 1 + 2
08:30 am - 09:00 am		
09:00 am - 09:30 am		
09:30 am - 10:00 am		
11:00 am - 12:30 pm	<b>■ Research Paper Session - Performance Modelling and Measurement II</b> <b>Chair:</b> <i>Saday Sadayappan, The Ohio State University</i> Applicability of the ECM Performance Model to Explicit ODE Methods on Current Multi-Core Processors <i>Johannes Seiferth, University of Bayreuth</i> Machine Learning Based Parallel I/O Predictive Modeling: A Case Study on Lustre File Systems <i>Sandeep Madireddy, Argonne National Laboratory</i> A Novel Multi-Level integrated Roofline Model Approach for Performance Characterization <i>Tuomas Koskela, Lawrence Berkeley National Laboratory, NERSC</i>	Substanz 1 + 2
11:00 am - 11:30 am		
11:30 am - 12:00 pm		
12:00 pm - 12:30 pm		
01:45 pm - 02:45 pm	<b>■ BoF: 16th Graph500 List</b> <b>Speakers:</b> <i>Richard Murphy, Micron Technology Inc., Boise State University</i> <i>David Bader, Georgia Tech</i> <i>Peter Kogge, University of Notre Dame</i> <i>Andrew Lumsdaine, Pacific Northwest National Laboratory</i>	Substanz 1 + 2

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**02:45 pm - 03:45 pm**      **■ BoF: The Computational Biomedicine Community and the HPC Industry: Working together to advance Personalised Medicine**      Substanz 1 + 2

*Speakers:*  
*Xavier Vigouroux, Atos / BULL SAS*  
*Benjamin Pajot, Atos / BULL SAS*  
*Myrtille Deldossi, Atos / BULL SAS*  
*Marcin Ostasz, ETP4HPC, European HPC Technology Platform; BSC*  
*Cristin Meritt, Alces Flight Limited*  
*Peter Coveney, University College of London*  
*Mariano Vázquez, Barcelona Supercomputing Center*  
*Gavin Pringle, EPCC, University of Edinburgh*  
*Marco Verdicchio, SURFsara*

**03:45 pm - 04:45 pm**      **■ BoF: Sharing Hybrid Cloud Computing Strategies in HPC**      Substanz 1 + 2

*Speakers:*  
*Fritz Ferstl, Univa*  
*Rob Lalonde, Univa*

**Pikkolo**

**08:30 am - 09:30 am**      **■ BoF: Reconfigurable Computing in HPC, ML and Data Analytics**      Pikkolo

*Speakers:*  
*Hans-Christian Hoppe, Intel*  
*Marie-Christine Sawley, Intel*

**09:30 am - 10:30 am**      **■ BoF: Unified Communication X (UCX)**      Pikkolo

*Speakers:*  
*Gilad Shainer, Mellanox*  
*Richard Graham, Mellanox*  
*Jeff Kuehn, Los Alamos National Laboratory*  
*Pavel Shamis, Arm*

**10:30 am - 11:30 am**      **■ BoF: HPC Outreach - Growing the Exascale Community**      Pikkolo

*Speakers:*  
*Nicholas Brown, EPCC*  
*Weronika Filinger, EPCC*  
*Martin Quinson, ENS Rennes*  
*Bryan Johnston, CHPC*

11:30 am - 12:30 pm	<p>■ <b>BoF: Retaining the Advantage of a diverse HPC Workforce: How to deal with Microaggression</b></p> <p><i>Speakers:</i>  <i>Toni Collis, Appentra, Collis-Holmes Innovations</i>  <i>Emmanouil Farsarakis, EPCC</i>  <i>Mozhgan Kabiri Chimeh, The University of Sheffield</i>  <i>Kelly Nolan, Talent Strategy</i></p>	Pikkolo
01:45 pm - 02:45 pm	<p>■ <b>BoF: Building Efficient Clouds for HPC, Big Data, and Deep Learning Middleware and Applications</b></p> <p><i>Speakers:</i>  <i>Dhabaleswar K. Panda, The Ohio State University</i>  <i>Xiaoyi Lu, The Ohio State University</i></p>	Pikkolo
02:45 pm - 03:45 pm	<p>■ <b>BoF: FFT in the Exascale: Opportunities and Challenges</b></p> <p><i>Speakers:</i>  <i>Samar Aseeri, King Abdullah University of Technology (KAUST)</i>  <i>Daisuke Takahashi, University of Tsukuba</i>  <i>Benson Muite, University of Tartu</i>  <i>Cris Cecka, Nvidia, Stanford University</i>  <i>Jens Henrik Göbbert, Jülich Supercomputing Centre (JSC)</i>  <i>Hari Subramoni, The Ohio State University</i>  <i>Peter Steinbach, Max Planck Institute of Molecular Cell Biology and Genetics, Scionics Computer Innovation GmbH</i></p>	Pikkolo
03:45 pm - 04:45 pm	<p>■ <b>BoF: PBS Pro Open Source Project Community BoF</b></p> <p><i>Speaker:</i>  <i>Bill Nitzberg, Altair</i></p>	Pikkolo

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Booth N-210

<b>10:20 am - 05:40 pm</b>	<b>■ Exhibitor Forum</b>	Booth N-210
10:20 am - 10:40 am	Quantum: Intelligent Data Management for Scientific Research <i>Jason Coari, Quantum Corporation</i>	
10:40 am - 11:00 am	Inspur: To be announced <i>To be announced</i>	
11:00 am - 11:20 am	D-Wave Systems: Software Tools for Quantum Computing Applications Development <i>Victoria Goliber, D-Wave Systems</i>	
11:20 am - 11:40 am	Quanta Cloud Technology (QCT): Simplify your HPC Implementation and Enhance Performance with QCT Solution <i>Theo Luecke, Quanta Cloud Technology (QCT)</i>	
11:40 am - 12:00 pm	Supermicro: Green HPC by Resource Saving <i>Martin Galle, Supermicro</i>	
12:00 pm - 12:20 pm	Mellanox: Super-Connecting the World's Number One Supercomputers <i>Scot Schultz, Mellanox Technologies</i>	
12:20 pm - 12:40 pm	Fujitsu: Fujitsu <i>Pierre Lagier, Fujitsu</i>	
12:40 pm - 01:00 pm	Bright Computing: Scalable Accounting & Reporting for Compute Jobs <i>Martijn de Vries, Bright Computing</i>	
01:00 pm - 01:20 pm	ClusterVision: Scalable HPC Storage That Delivers on Performance <i>Andy Georgi, ClusterVision</i>	
01:20 pm - 01:40 pm	Automation N.V: CERN's Reply to the Computing and Storage Challenges related to the Large Hadron Collider's (LHC) Upgrade Program: Prefabricated Datacenter Modules <i>Niko Neufeld, CERN - European Organization for Nuclear Research</i>	
01:40 pm - 02:00 pm	EXTOLL GmbH: Evaluation of Lustre RDMA Performance over EXTOLL <i>Sarah Neuwirth, University of Heidelberg</i>	
02:00 pm - 02:20 pm	Fraunhofer Institute for Computer Graphics Research (IGD): Open Call for Application Experiment <i>André Stork, Fraunhofer Institute for Computer Graphics Research (IGD)</i>	
02:20 pm - 02:40 pm	NEC Deutschland GmbH: NEC's Portfolio in HPC: Comprehensive Solutions for the Demanding Customer <i>Michael Wirth, NEC Deutschland GmbH</i>	
02:40 pm - 03:00 pm	NetApp: Optimize Scale-Out Architectures <i>Stan Skelton, NetApp</i>	

03:00 pm - 03:20 pm	Oracle: Run Cloud HPC and GPU Applications Without a Virtual Layer <i>Taylor Newill, Oracle</i>
03:20 pm - 03:40 pm	RSC Group: Hyper-converged and ultra-compact RSC Tornado solution for HPC, datacenter and cloud <i>Alexey Shmelev, RSC Group</i> <i>Alexander Moskovsky, RSC Group</i>
03:40 pm - 04:00 pm	Red Hat Inc.: To be announced <i>To be announced</i>
04:00 pm - 04:20 pm	SUSE: Enabling New Discoveries with SUSE Linux Enterprise for HPC <i>Jay Kruemcke, SUSE</i> <i>Jeff Reser, SUSE</i>
04:20 pm - 04:40 pm	One Stop Systems: Rack Scale Composable Infrastructure for Mixed Workload Datacenters <i>Jaen Mannik, One Stop Systems</i>
04:40 pm - 05:00 pm	Rausch Netzwerktechnik GmbH: To be announced <i>To be announced</i>
05:00 pm - 05:20 pm	Penguin Computing: To be announced <i>To be announced</i>

#### Booth N-230

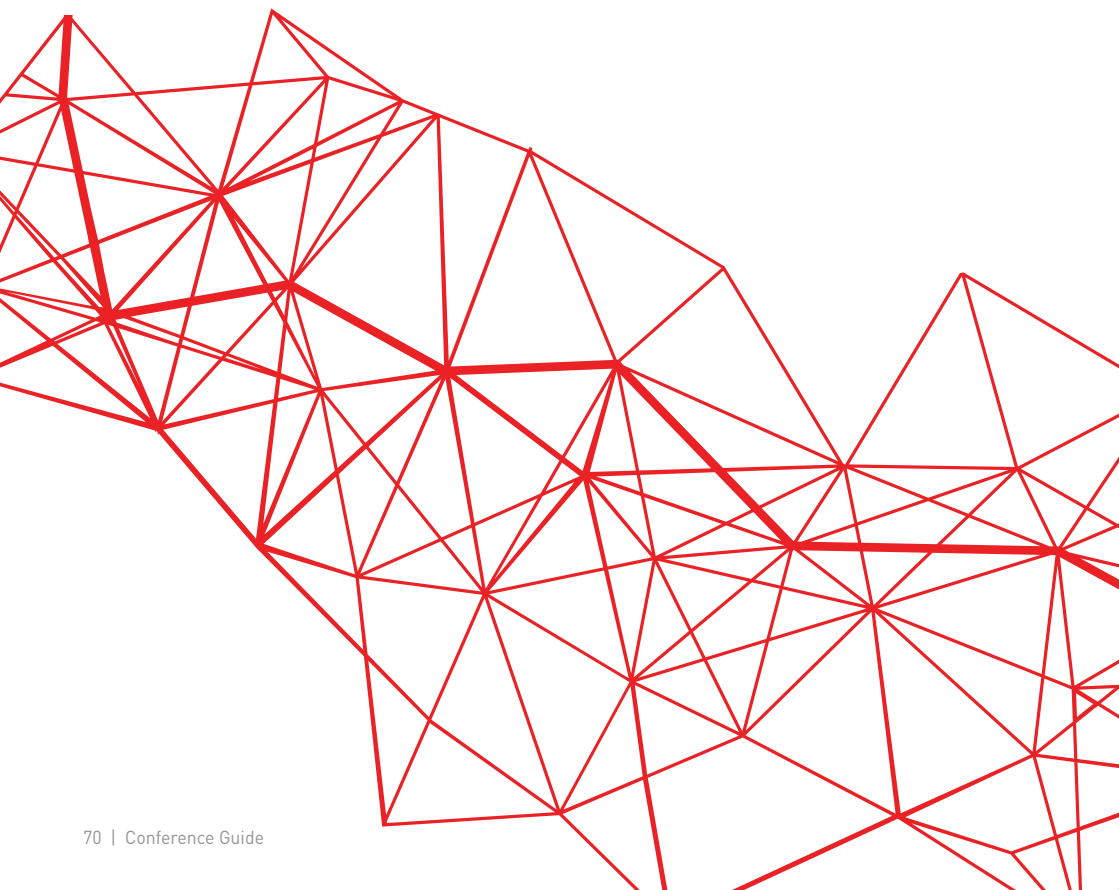
<b>10:00 am - 06:00 pm</b>	 <b>Project Posters</b> For a complete list of the Project Posters on display at ISC 2018, please refer to page 52.	Booth N-230
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#### Exhibition Hall

<b>10:00 am - 06:00 pm</b>	 <b>Exhibition</b>	Exhibition Hall
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#### Coffee & Lunch Breaks

08:00 am - 09:00 am	Welcome Coffee	Foyer Hall 3
10:00 am - 11:00 am	Coffee Break	Exhibition Hall
12:30 pm - 01:45 pm	Lunch Break	Exhibition Hall
03:15 pm - 04:00 pm	Coffee Break	Panorama Foyer, Exhibition Hall
04:45 pm - 05:15 pm	Coffee Break	Exhibition Hall
05:00 pm - 05:30 pm	Coffee Break	Panorama Foyer



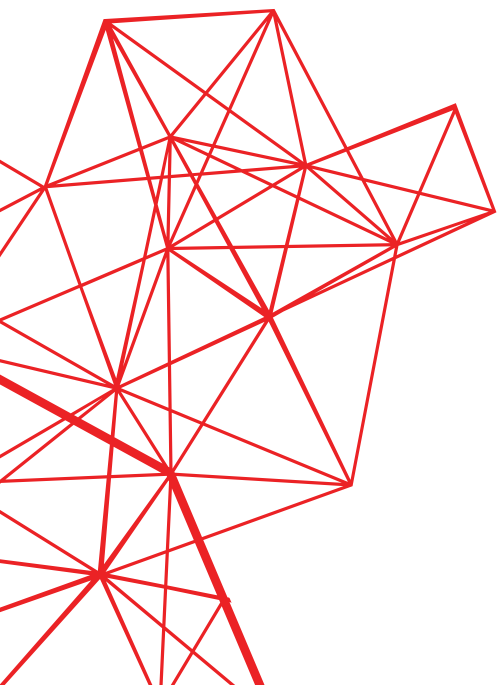
# PROGRAM

## Workshops

(in chronological order per room)

# THURSDAY, JUNE 28

The workshops will be held at the  
Frankfurt Marriott Hotel  
(Hamburger Allee 2, 60486 Frankfurt am Main)



ROOM (Marriott)	8am	9am	10am	11am	12pm	1pm	2pm	3pm	4pm	5pm	6pm	7pm	8pm	9pm
Platinum 2 (1st Floor)						Going ARM for HPC								
Gold 1 (1st Floor)							HPC I/O in the Data Center - 4th HPC-IODC Workshop							
Gold 2 (1st Floor)							Workshop on Performance & Scalability of Storage Systems (WOPSSS) 18							
Gold 3 (1st Floor)							First Workshop on the Convergence of Large Scale Simulation and Artificial Intelligence			3rd Workshop for Open Source Supercomputing (OpenSuCo)				
Lava 1 + 2 (2nd Floor)							High Performance Container Workshop							
Flint (2nd Floor)							3rd International Workshop on In Situ Visualization: Introduction and Applications							
Basalt (2nd Floor)							ExaComm: Fourth International Workshop on Communication Architectures for HPC, Big Data, Deep Learning and Clouds at Extreme Scale							
Alabaster 1 (2nd Floor)							Second ISC HPC Applications in Precision Medicine Workshop							
Alabaster 2 (2nd Floor)						Women in HPC: Diversifying the HPC Community				First Workshop on Interactive High-Performance Computing				
Matrix (5th Floor)						Many-core Computing on Intel Processors: Applications, Performance and Best-Practice Solutions								
Kilobyte (5th Floor)						13th Workshop on Virtualization in High-Performance Cloud Computing (VHPC '18)								
Megabyte (5th Floor)						2nd Workshop on HPC Computing in a Post Moore's Law World (HPCPM) 2018				Post Moore Interconnects				
Gigabyte (5th Floor)						X86, ARM, GPUs, Oh My! Today's Programming Environment for the Complex, Many-choice Platforms in HPC				Workshop on Performance Portable Programming Models for Accelerators (P <sup>3</sup> JMA)				
Ampere (5th Floor)						International Workshop on OpenPOWER for HPC (IWOPH'18)								
Lux (5th Floor)						Approximate and Transprecision Computing on Emerging Technologies (ATCET)				The Power of Losing Control - When does a re-implementation of mature simulation fragments with HPC DSLs pay off?				
Candela (5th Floor)						Workshop on Sustainable Ultrascale Computing Systems								
Volt (5th Floor)						2nd Workshop on HPC Collaboration between Europe and Latin America				Energy Efficiency Tools for High Performance Computing - EEHPC				
<div><div></div> CONFERENCE PASS</div> <div><div></div> EXHIBITION PASS</div> <div><div></div> TUTORIAL PASS</div> <div><div></div> WORKSHOP PASS</div> <div>Program may be subject to changes; the latest version of the program is available at <a href="https://2018.isc-program.com/">https://2018.isc-program.com/</a></div>														



Platinum 2 (1st Floor)		
09:00 am - 06:00 pm	<div>■ <b>GoingARM for HPC</b></div> <div><i>Organizers:</i> <i>Jonathan Beard, Arm Inc.</i> <i>Roxana Rusitoru, Arm Ltd.</i> <i>Oscar Hernandez, Oakridge National Labs</i> <i>Mitsuhisa Sato, RIKEN</i> <i>Simon McIntosh-Smith, University of Bristol</i> <i>Filippo Mantovani, Barcelona Supercomputing Center</i> <i>Robert Hoekstra, Sandia National Laboratories</i></div> <div>For more details, please visit the workshop website at: <a href="http://www.goingarm.com">http://www.goingarm.com</a></div>	Platinum 2
Gold 1 (1st Floor)		
09:00 am - 06:00 pm	<div>■ <b>HPC I/O in the Data Center - 4th HPC-IODC Workshop</b></div> <div><i>Organizers:</i> <i>Julian Kunkel, University of Reading</i> <i>Jay Lofstead, Sandia National Laboratories</i></div> <div>For more details, please visit the workshop website at: <a href="https://hps.vi4io.org/events/2018/iodc">https://hps.vi4io.org/events/2018/iodc</a></div>	Gold 1
Gold 2 (1st Floor)		
09:00 am - 06:00 pm	<div>■ <b>Workshop on Performance &amp; Scalability of Storage Systems (WOPSSS) 18</b></div> <div><i>Organizer:</i> <i>Jean-Thomas Acquaviva, DDN</i></div> <div>For more details, please visit the workshop website at: <a href="http://wopsss.com">http://wopsss.com</a></div>	Gold 2

**Gold 3 (1st Floor)**

- 09:00 am - 01:00 pm**      **■ First Workshop on the Convergence of Large Scale Simulation and Artificial Intelligence**      Gold 3  
*Organizers:*  
*Christoph Angerer, NVIDIA*  
*Axel Koehler, NVIDIA*  
 For more details, please visit the workshop website at:  
<https://sites.google.com/nvidia.com/ai-hpc>
- 02:00 pm - 06:00 pm**      **■ 3rd Workshop for Open Source Supercomputing (OpenSuCo)**      Gold 3  
*Organizers:*  
*Farzad Fatollahi-Fard, Lawrence Berkeley National Laboratory*  
*David Donofrio, Lawrence Berkeley National Laboratory*  
*John Leidel, Tactical Computing Laboratories, Texas Tech University*  
*Anastasiia Butko, Lawrence Berkeley National Laboratory*  
*Sven Karlsson, Technical University of Denmark*  
 For more details, please visit the workshop website at:  
<http://www.opensuco.community/>

**Alabaster 1 (2nd Floor)**

- 09:00 am - 06:00 pm**      **■ Second ISC HPC Applications in Precision Medicine Workshop**      Alabaster 1  
*Organizers:*  
*Thomas Steinke, Zuse Institute Berlin*  
*Sunita Chandrasekaran, University of Delaware*  
*Patricia Kovatch, Mount Sinai Icahn School of Medicine*  
*Eric Stahlberg, Frederick National Laboratory for Cancer Research*  
 For more details, please visit the workshop website at:  
<http://www.scworkshops.net/precision2018>

Alabaster 2 (2nd Floor)		
09:00 am - 01:00 pm	<p>■ <b>Women in HPC: Diversifying the HPC Community</b></p> <p><i>Organizers:</i>  <i>Elsa Gonsiorowski, Lawrence Livermore National Laboratory</i>  <i>Toni Collis, Appentra Solutions S.L., Collis-Holmes Innovations</i>  <i>Misbah Mubarak, Argonne National Laboratory</i></p> <p>For more details, please visit the workshop website at:  <a href="http://www.womeninhpc.org/whpc-isc18/workshop">http://www.womeninhpc.org/whpc-isc18/workshop</a></p>	Alabaster 2
02:00 pm - 06:00 pm	<p>■ <b>First Workshop on Interactive High-Performance Computing</b></p> <p><i>Organizers:</i>  <i>Peter Messmer, NVIDIA</i>  <i>Michael Ringenburt, Cray Inc.</i></p> <p>For more details, please visit the workshop website at:  <a href="https://sites.google.com/view/interative-hpc/home">https://sites.google.com/view/interative-hpc/home</a></p>	Alabaster 2
Flint (2nd Floor)		
09:00 am - 06:00 pm	<p>■ <b>3rd International Workshop on In Situ Visualization: Introduction and Applications</b></p> <p><i>Organizers:</i>  <i>Kenneth Moreland, Sandia National Laboratories</i>  <i>Guido Reinà, University of Stuttgart</i>  <i>Thomas Theussl, KAUST</i>  <i>Tom Vierjahn, RWTH Aachen University</i></p> <p>For more details, please visit the workshop website at:  <a href="http://woiv.org/">http://woiv.org/</a></p>	Flint
Basalt (2nd Floor)		
09:00 am - 06:00 pm	<p>■ <b>ExaComm: Fourth International Workshop on Communication Architectures for HPC, Big Data, Deep Learning and Clouds at Extreme Scale</b></p> <p><i>Organizers:</i>  <i>Hari Subramoni, The Ohio State University</i>  <i>Dhabaleswar Panda, The Ohio State University</i></p> <p>For more details, please visit the workshop website at:  <a href="http://nowlab.cse.ohio-state.edu/exacomm/">http://nowlab.cse.ohio-state.edu/exacomm/</a></p>	Basalt

General Information

Sunday, June 24

Monday, June 25

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**Lava 1 + 2 (2nd Floor)**

09:00 am - 06:00 pm	<p><b>High Performance Container Workshop</b></p> <p><i>Organizer:</i>  <b>Christian Kniep, Docker Inc.</b></p> <p>For more details, please visit the workshop website at:  <a href="http://qnib.org/isc/">http://qnib.org/isc/</a></p>	Lava 1 + 2
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**Kilobyte (5th Floor)**

09:00 am - 06:00 pm	<p><b>13th Workshop on Virtualization in High-Performance Cloud Computing (VHPC '18)</b></p> <p><i>Organizers:</i>  <b>Michael Alexander, IST Austria</b>  <b>Anastassios Nanos, National Technical University of Athens (NTUA), Onapp Ltd.</b>  <b>Romeo Kienzler, IBM</b></p> <p>For more details, please visit the workshop website at:  <a href="http://vhpc.org">http://vhpc.org</a></p>	Kilobyte
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**Megabyte (5th Floor)**

09:00 am - 01:00 pm	<p><b>2nd Workshop on HPC Computing in a Post Moore's Law World (HCPM) 2018</b></p> <p><i>Organizers:</i>  <b>George Michelogiannakis, LBNL</b>  <b>Jeff Vetter, ORNL, Georgia Institute of Technology</b></p> <p>For more details, please visit the workshop website at:  <a href="http://workshops.postmoore.org/hcpm2018/">http://workshops.postmoore.org/hcpm2018/</a></p>	Megabyte
02:00 pm - 06:00 pm	<p><b>Post Moore Interconnects</b></p> <p><i>Organizers:</i>  <b>Neena Imam, Oak Ridge National Laboratory</b>  <b>Barney Maccabe, Oak Ridge National Laboratory</b>  <b>Sebastien Le Beux, University of Lyon</b>  <b>Torsten Hoefler, ETH Zurich</b>  <b>William Harrod, IARPA</b></p> <p>For more details, please visit the workshop website at:  <a href="https://beyonddcmos.ornl.gov/2018/">https://beyonddcmos.ornl.gov/2018/</a></p>	Megabyte

Gigabyte (5th Floor)			General Information
09:00 am - 01:00 pm	<p>■ <b>X86, ARM, GPUs, Oh My! Today's Programming Environment for the Complex, Many-choice Platforms in HPC</b></p> <p><i>Organizer:</i>  <b>Luiz DeRose, Cray Inc.</b></p> <p>For more details, please visit the workshop website at:  <a href="https://pe-isc.github.io/workshop">https://pe-isc.github.io/workshop</a></p>	Gigabyte	
02:00 pm - 06:00 pm	<p>■ <b>Workshop on Performance Portable Programming Models for Accelerators (P<sup>3</sup>MA)</b></p> <p><i>Organizers:</i>  <b>Sunita Chandrasekaran, University of Delaware</b>  <b>Graham Lopez, Oak Ridge National Laboratory</b></p> <p>For more details, please visit the workshop website at:  <a href="http://www.csm.ornl.gov/workshops/p3ma2018/">http://www.csm.ornl.gov/workshops/p3ma2018/</a></p>	Gigabyte	Sunday, June 24
Matrix (5th Floor)			Monday, June 25
09:00 am - 06:00 pm	<p>■ <b>Many-core Computing on Intel Processors: Applications, Performance and Best-Practice Solutions</b></p> <p><i>Organizers:</i>  <b>David E. Martin, Argonne National Laboratory</b>  <b>Simon J. Pennycook, Intel</b>  <b>Thomas Steinke, Zuse Institute Berlin</b>  <b>Estela Suarez, Jülich Supercomputing Centre (JSC)</b></p> <p>For more details, please visit the workshop website at:  <a href="https://www.ixpug.org/events/ixpug-isc-2018-workshop">https://www.ixpug.org/events/ixpug-isc-2018-workshop</a></p>	Matrix	Tuesday, June 26
Ampere (5th Floor)			Wednesday, June 27
09:00 am - 06:00 pm	<p>■ <b>International Workshop on OpenPOWER for HPC (IWOPH'18)</b></p> <p><i>Organizers:</i>  <b>Jack Wells, Oak Ridge National Laboratory</b>  <b>Dirk Pleiter, Jülich Supercomputing Centre (JSC)</b></p> <p>For more details, please visit the workshop website at:  <a href="https://indico-jsc.fz-juelich.de/event/64/">https://indico-jsc.fz-juelich.de/event/64/</a></p>	Ampere	Thursday, June 28

**Volt** (5th Floor)

- 09:00 am - 01:00 pm** **■ 2nd Workshop on HPC Collaboration between Europe and Latin America** Volt
- Organizers:*  
*Carlos Jaime Barrios Hernandez, Advanced Computing Services for Latin America and Caribbean, Universidad Industrial de Santander*  
*Pascal Bouvry, Luxembourg University*  
*Rafael Mayo, RICAP - Iberoamerican Network for HPC, CIEMAT - Center For Research in Energy and Environment of Spain*  
*Ulises Cortes, Barcelona Supercomputing Center, Universidad Polit cnica de Catalunya*  
*Salma Jaliffe, Advanced Computing Services for Latin America and Caribbean, Corporaci n Universitaria de Internet*  
For more details, please visit the workshop website at:  
<http://www.sc3.uis.edu.co/2ndwhpceurolatam/>
- 02:00 pm - 06:00 pm** **■ Energy Efficiency Tools for High Performance Computing – EETHPC** Volt
- Organizers:*  
*Robert Sch ne, Technische Universit t Dresden*  
*Jo o M.P. Cardoso, University of Porto*  
*Barry L. Rountree, Lawrence Livermore National Laboratory*  
For more details, please visit the workshop website at:  
<http://www.eethpc.net>

**Candela** (5th Floor)

- 09:00 am - 06:00 pm** **■ Workshop on Sustainable Ultrascale Computing Systems** Candela
- Organizer:*  
*Jesus Carretero, University Carlos III of Madrid*  
For more details, please visit the workshop website at:  
<http://www.nesus.eu/workshop-sustainable-ultrascale-computing-systems/>

Lux

**09:00 am - 01:00 pm** Lux

■ **Approximate and Transprecision Computing on Emerging Technologies (ATCET)**

*Organizers:*  
*Costas Bekas, IBM Research - Zurich*  
*A. Cristiano I. Malossi, IBM Research - Zurich*  
*Luca Benini, ETH Zurich*  
*Enrique S. Quintana-Ortí, Universidad Jaime I*  
*Dimitrios S. Nikolopoulos, Queen's University*  
 For more details, please visit the workshop website at:  
<http://oprecomp.eu/atcet>

**02:00 pm - 06:00 pm** Lux

■ **The Power of Losing Control – When does a re-implementation of mature simulation fragments with HPC DSLs pay off?**

*Organizers:*  
*Tobias Weinzierl, Durham University*  
*Michael Bader, Technical University of Munich*  
 For more details, please visit the workshop website at:  
<https://community.dur.ac.uk/tobias.weinzierl/workshops.html>

Coffee & Lunch Breaks

08:30 am - 09:00 am	Welcome Coffee	Foyers
11:00 am - 11:30 am	Coffee Break	Foyers
01:00 pm - 02:00 pm	Lunch Break	Platinum 1, Platinum Foyer
04:00 pm - 04:30 pm	Coffee Break	Foyers

General Information

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## NOTES



NOTES

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## NOTES

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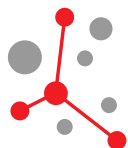
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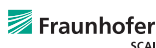
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