ISC 2021 Call for Tutorials

PLEASE NOTE: Accepted Tutorials from ISC 2020 will be held at ISC 2021.

The ISC Tutorials are interactive courses focusing on key topics of high performance computing, networking, storage, and data science. Renowned experts in their respective fields will give attendees a comprehensive introduction to the topic as well as providing a closer look at specific problems. Tutorials are encouraged to include a “hands-on” component to allow attendees to practice prepared materials.

The Tutorials will be held on Thursday, June 24, and on Friday, June 25, 2021.

The ISC 2021 Tutorials Committee is headed by Kevin Huck, University of Oregon, USA, with Kathryn Mohror, Lawrence Livermore National Laboratory, USA, as Deputy Chair.

Accepted Tutorials

- Compression for Scientific & Engineering Data
- OpenMP Common Core: Learning Parallelization of Real Applications from the Ground-Up
- The Scalable Vector Extension: Programming Tools and Performance Analysis
- Productive Parallel Programming for FPGA with High-Level Synthesis
- Mastering Tasking with OpenMP
- Hands-On HPC Application Development Using C++ and SYCL
- Determining Parallel Application Execution Efficiency and Scaling using the POP Methodology
- InfiniBand, High-speed Ethernet, and RoCE for Beginners
- High Performance Distributed Deep Learning
- Hands-on Practical Hybrid Parallel Application Performance Engineering
- Modern Mixed- and Multi-Precision Methods
- Getting Started with Containers on HPC
- Maintaining a Modern Scientific Software Stack Made Easy with EasyBuild
- Better Scientific Software
- Managing HPC Software Complexity with Spack
- Kokkos: Performance Portability for C++ Applications and Libraries
- Introduction to HPC: Applications, Systems, and Programming Models
- Advanced MPI Programming
Chair - Kevin Huck

ISC 2021 Tutorials Committee

- **Kevin Huck**, University of Oregon, United States of America (Chair)
- **Kathryn Mohror**, Lawrence Livermore National Laboratory, United States of America (Deputy Chair)
- **Damian Alvarez**, Forschungszentrum Juelich GmbH, Germany
- **Katie Antypas**, Lawrence Berkeley National Laboratory, United States of America
- **Rosa M. Badia**, Barcelona Supercomputing Center, Spain
- **Pavan Balaji**, Argonne National Laboratory, United States of America
- **Jong Choi**, Oak Ridge National Laboratory, United States of America
- **Dan Ellsworth**, Colorado College, United States of America
- **Mozhgan Kabiri Chimeh**, NVIDIA, United Kingdom
- **Michael O. Lam**, James Madison University, Lawrence Livermore National Laboratory, United States of America
- **David Lecomber**, Arm, Arm Ltd, United Kingdom
- **Kelvin Li**, IBM, Canada
- **Simon McIntosh-Smith**, University of Bristol, Prometeus GmbH, United Kingdom
- **CJ Newburn**, NVIDIA, United States of America
- **Dhabaleswar Panda**, Ohio State University, United States of America
- **Ojas Parekh**, SNL, United States of America
- **Olga Pearce**, Lawrence Livermore National Laboratory, United States of America
- **Christian Plessl**, Paderborn University, Germany
- **Harald Servat**, Intel, Spain
- **Michela Taufer**, University of Delaware, United States of America